

1/25

FIG. 1

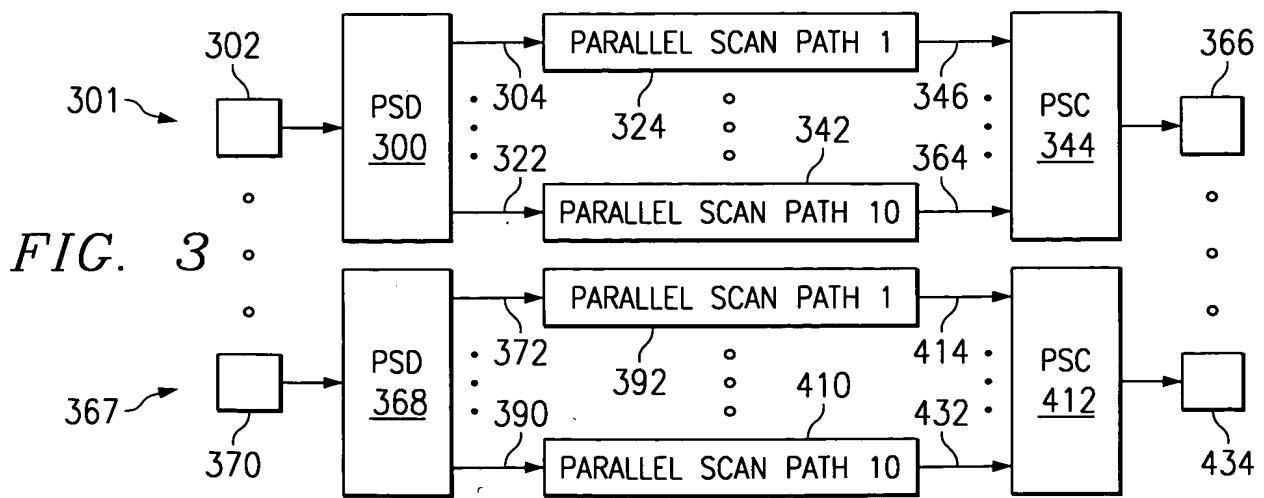
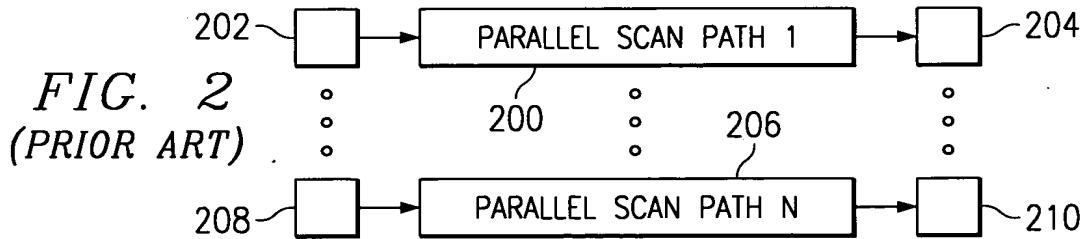
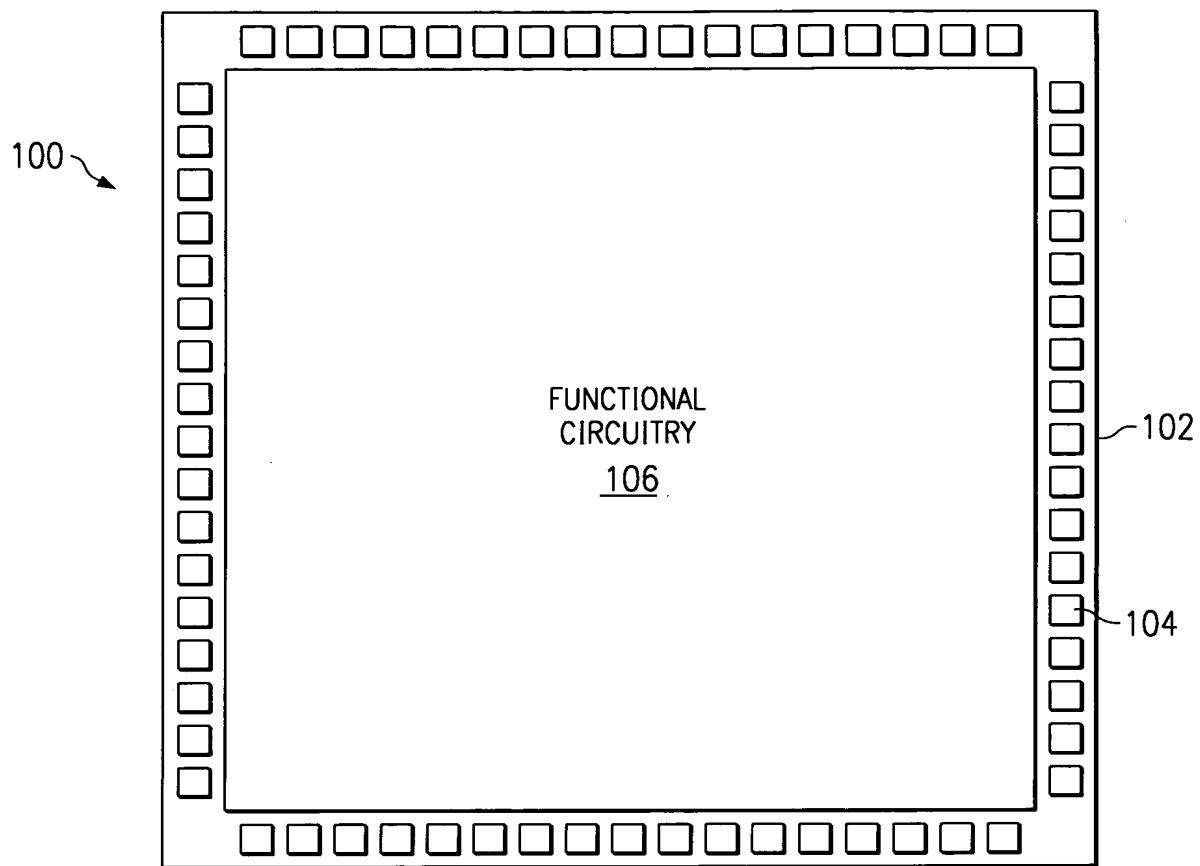


FIG. 4

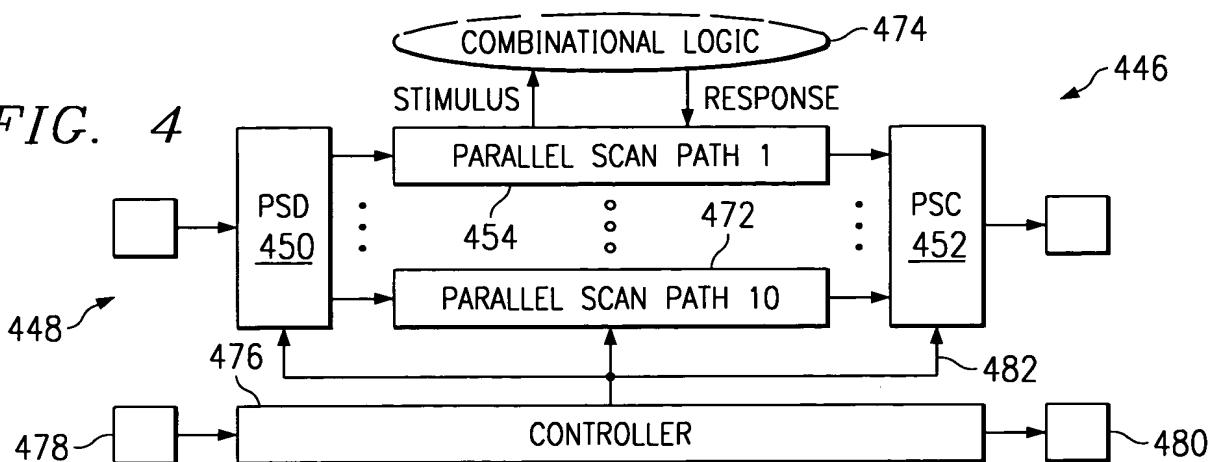
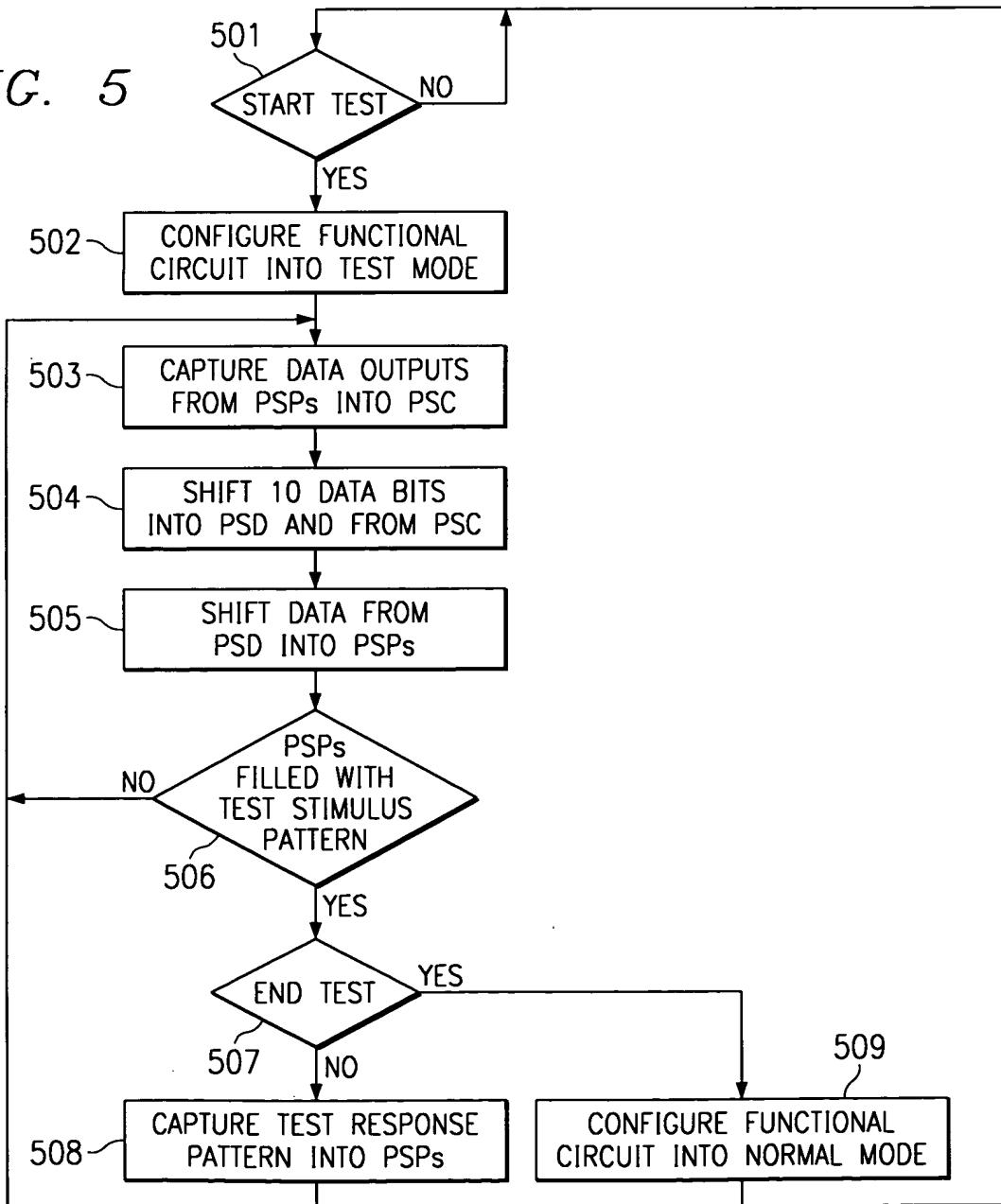


FIG. 5



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FIG. 6

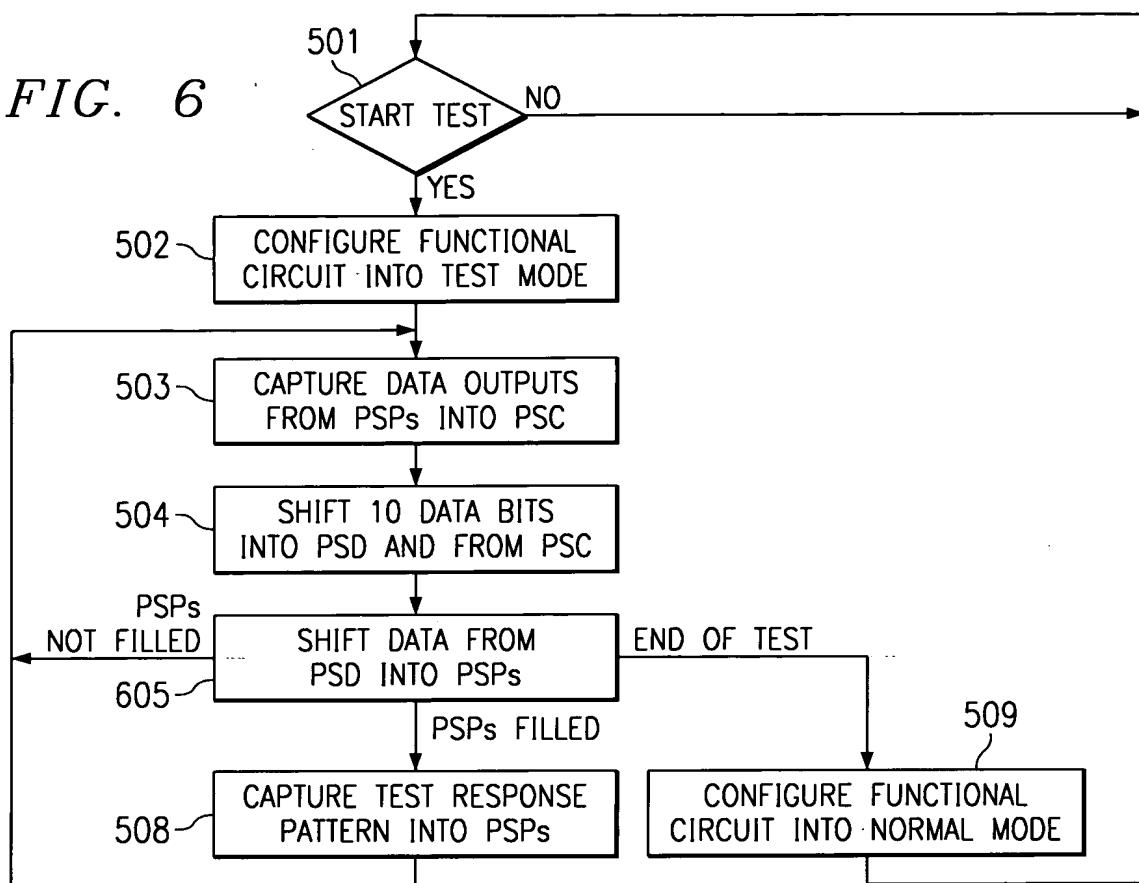
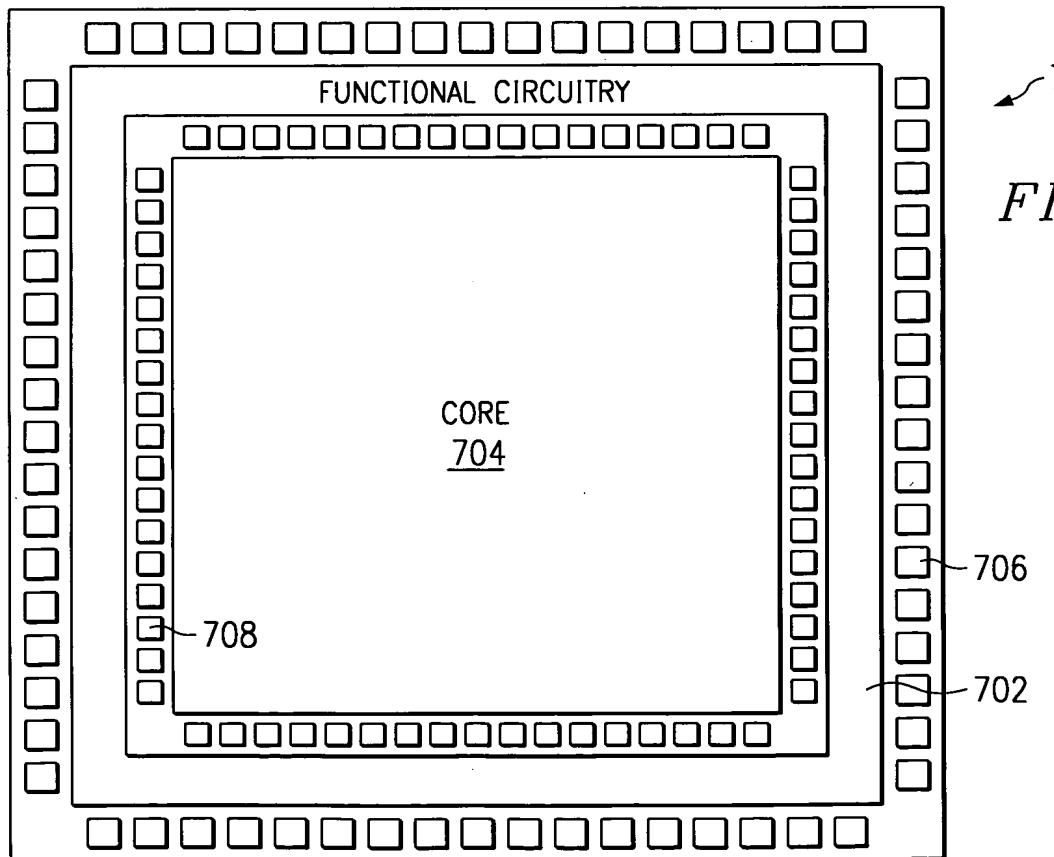


FIG. 7



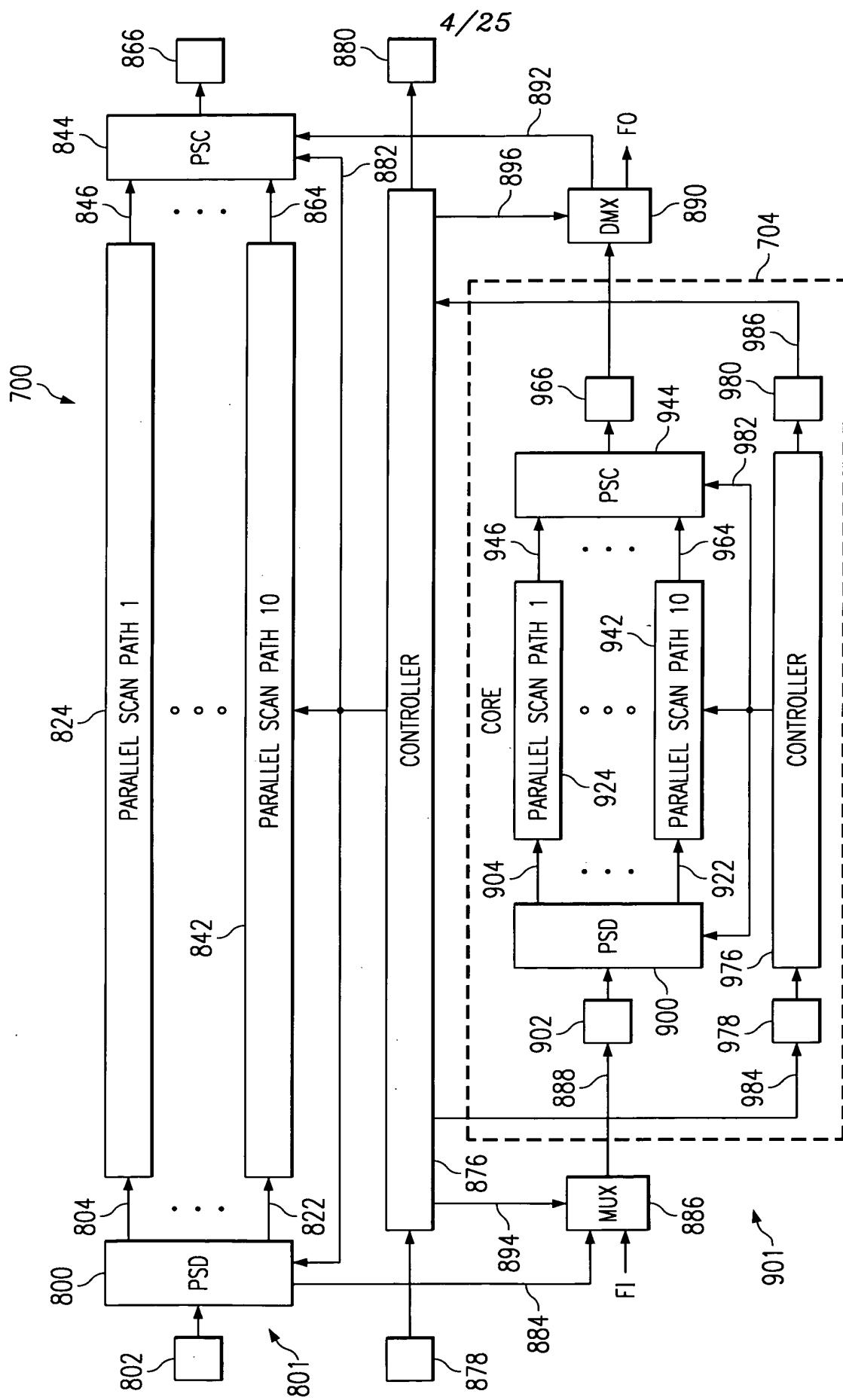


FIG. 8

FIG. 9

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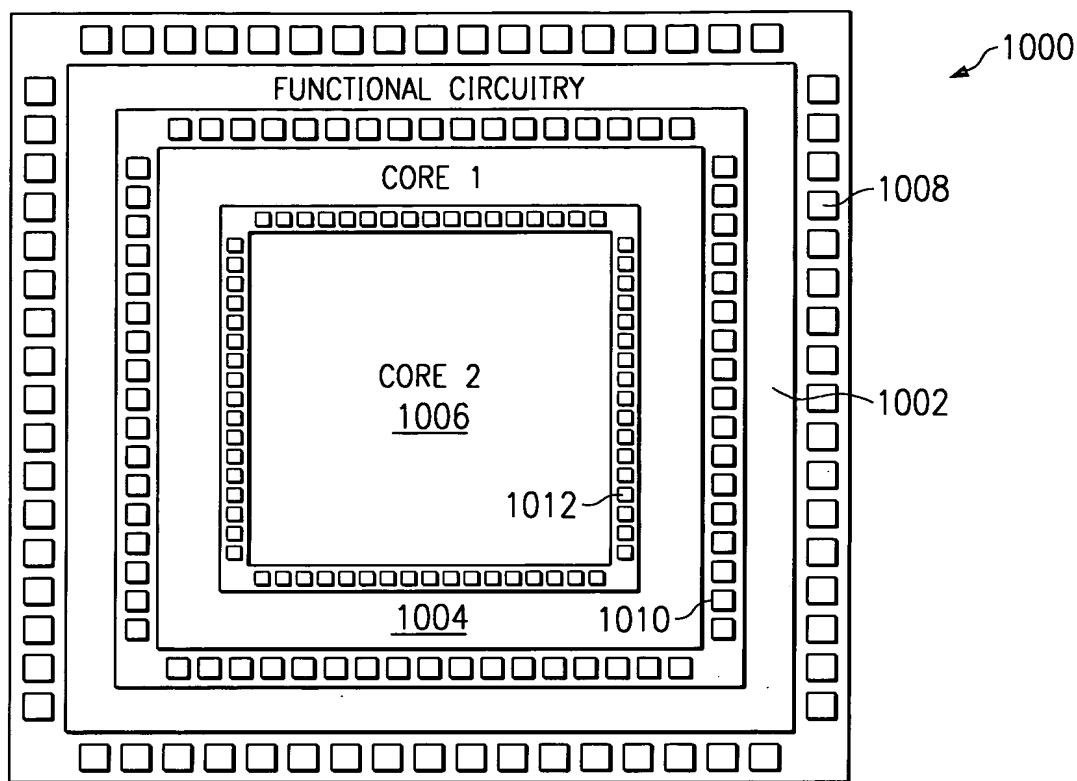
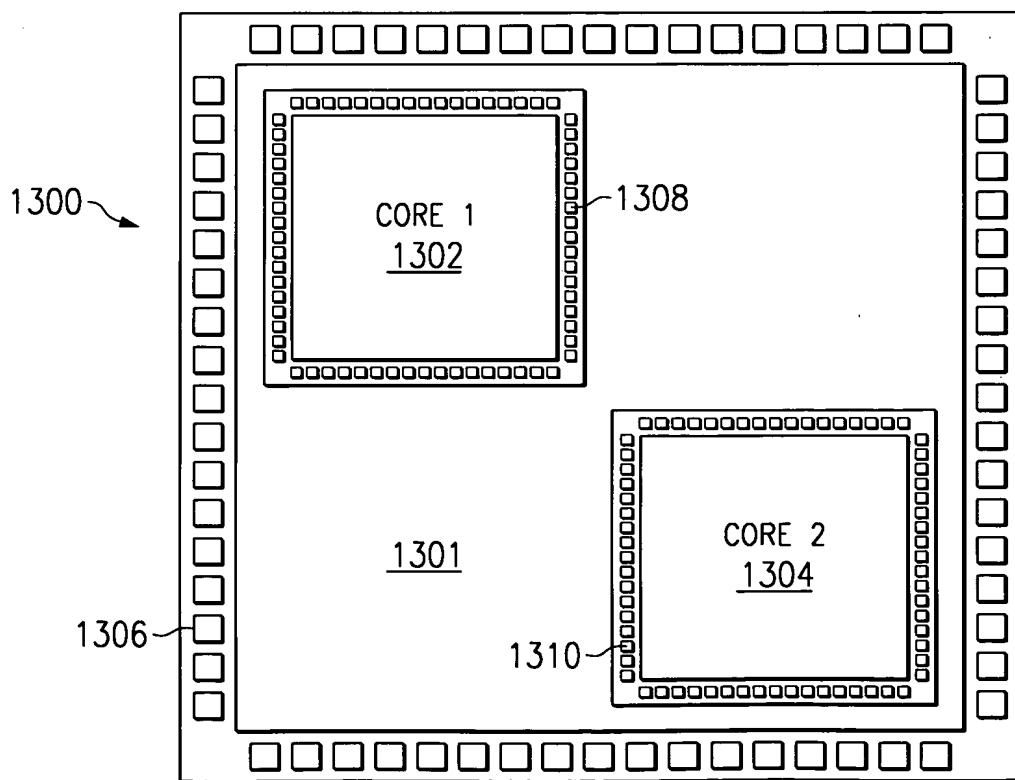
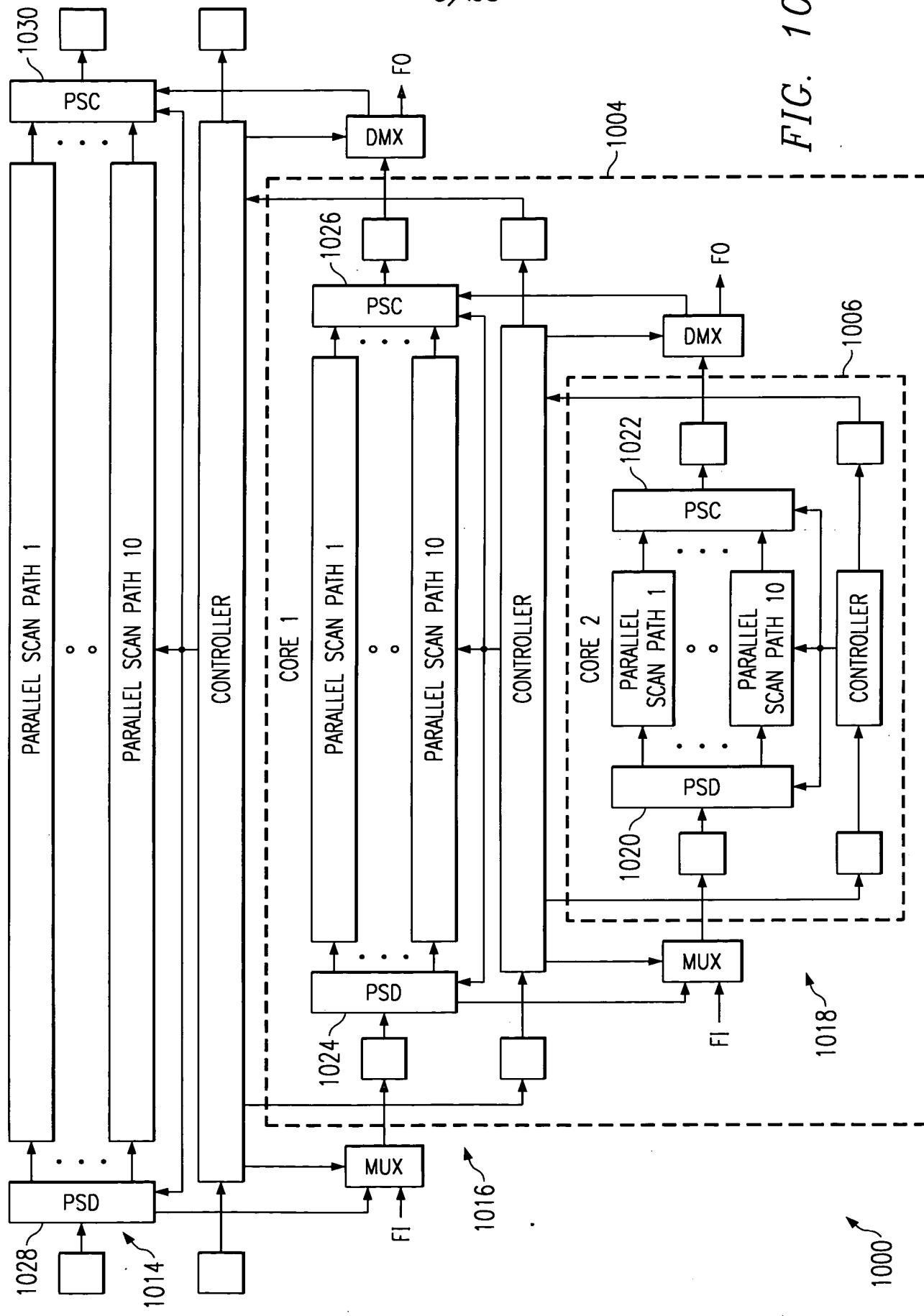


FIG. 13





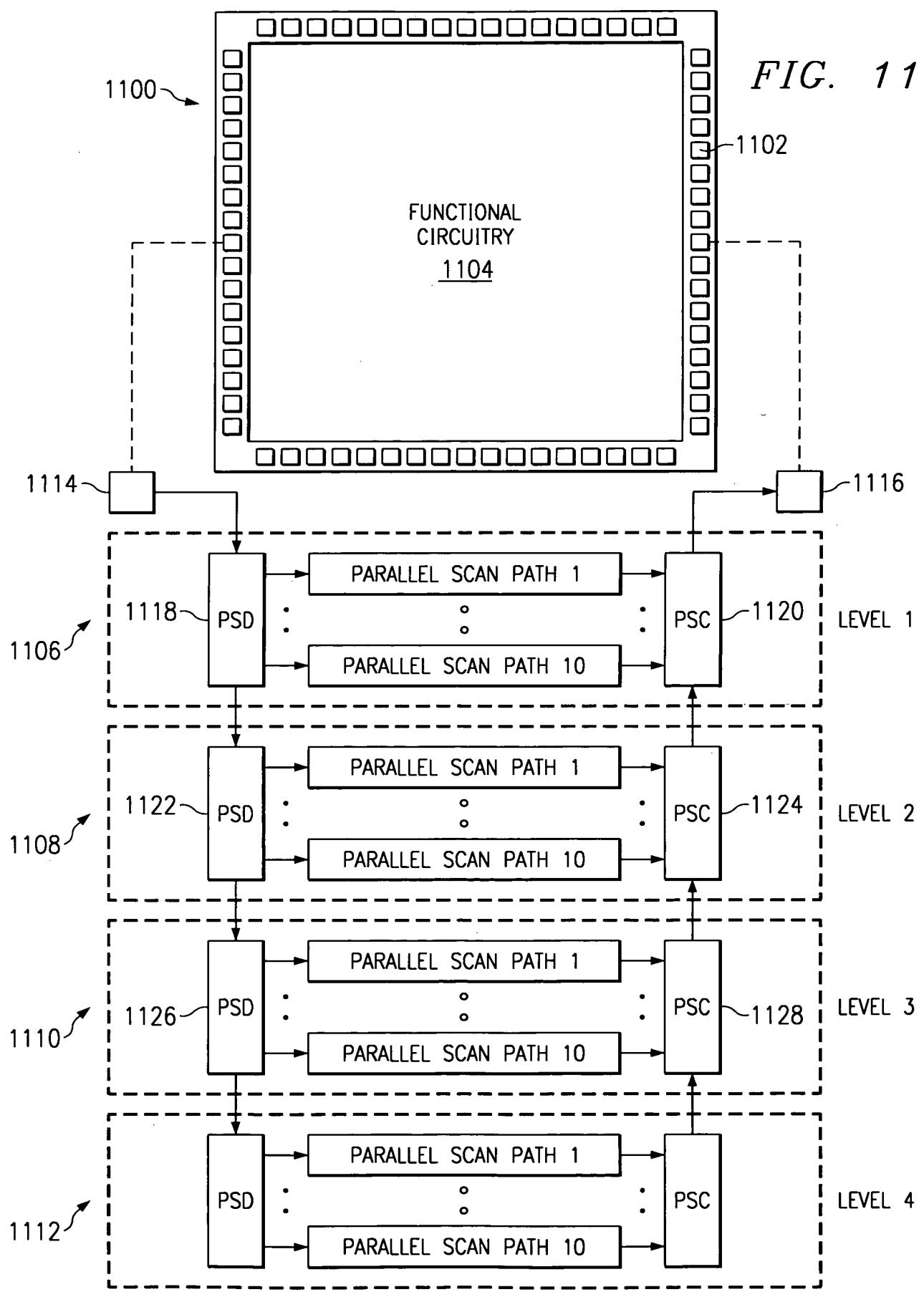
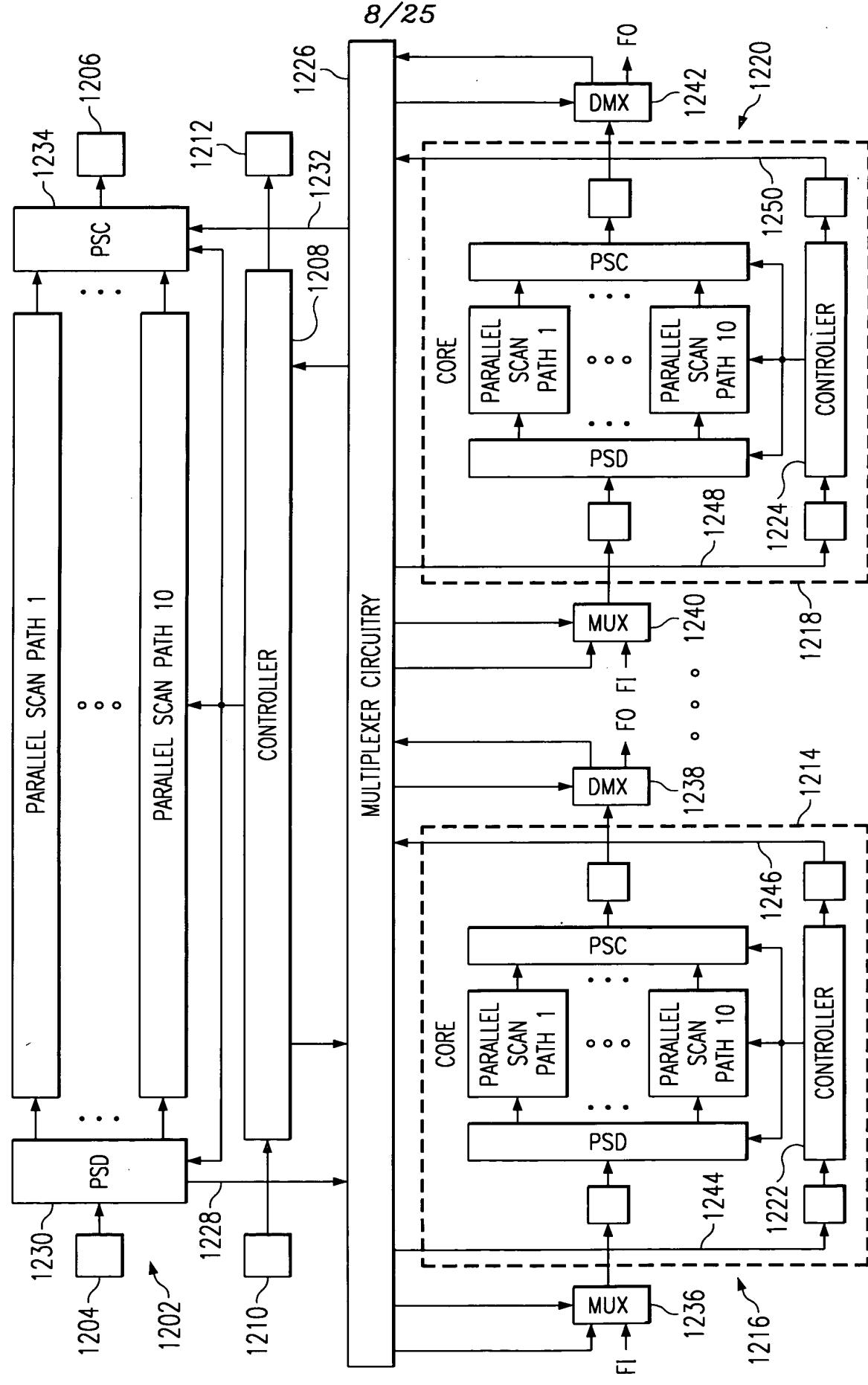
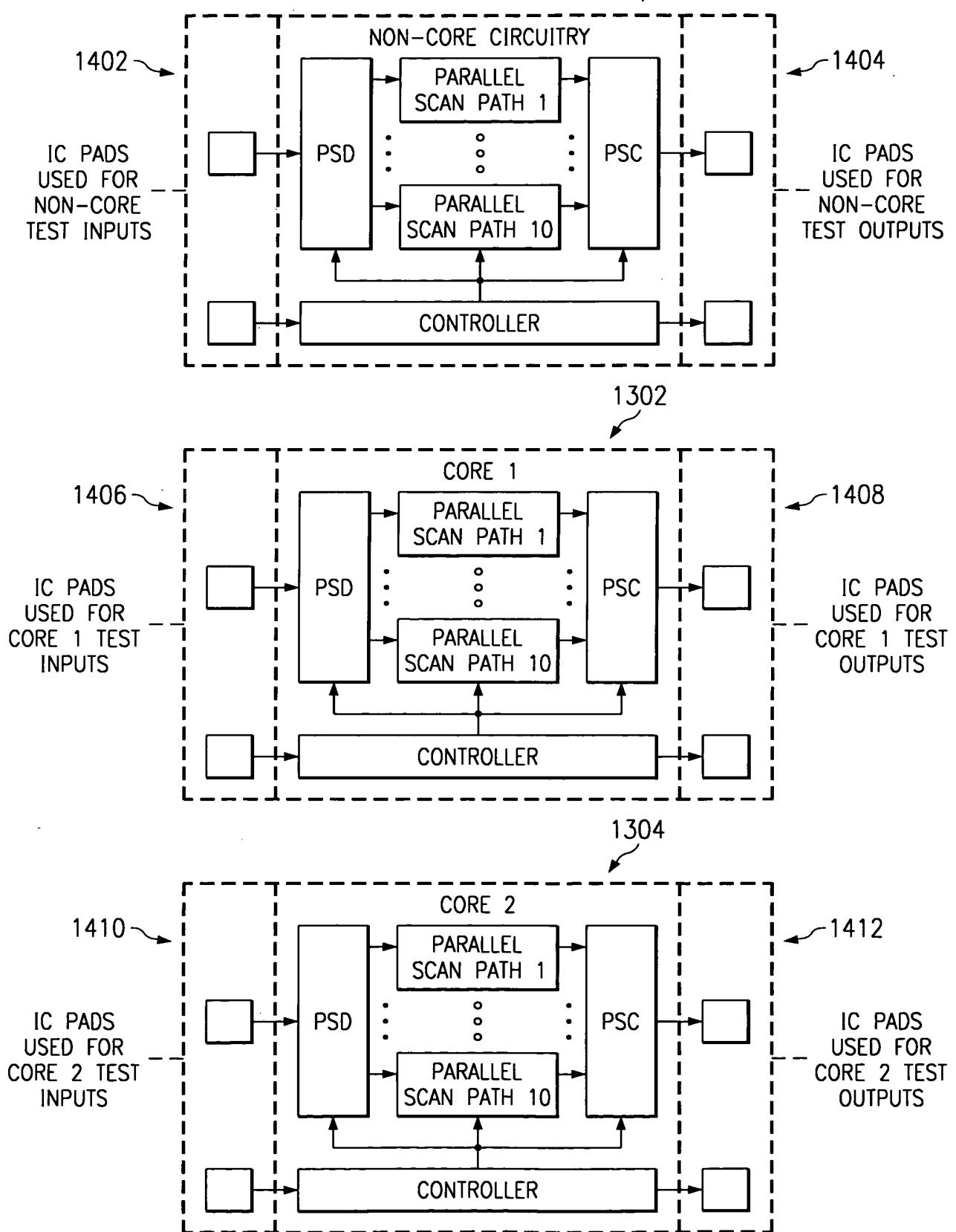


FIG. 12



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FIG. 14



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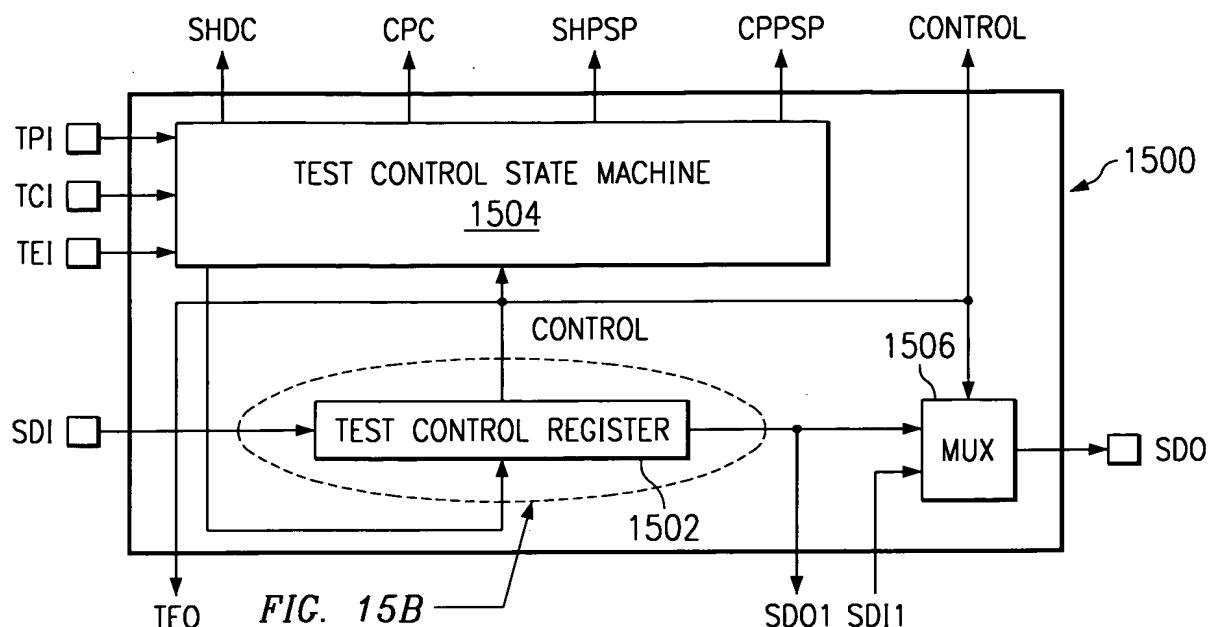


FIG. 15A

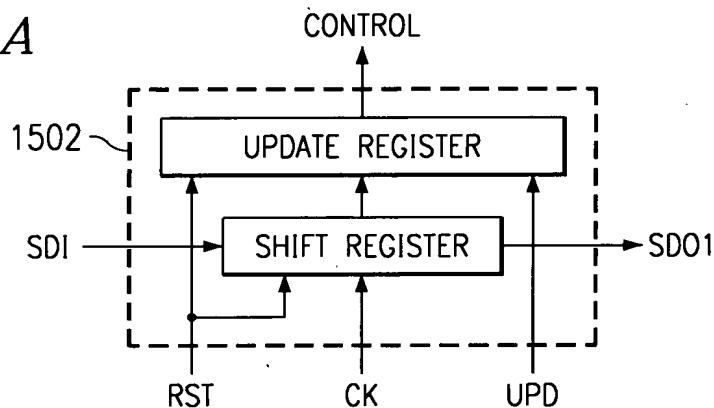
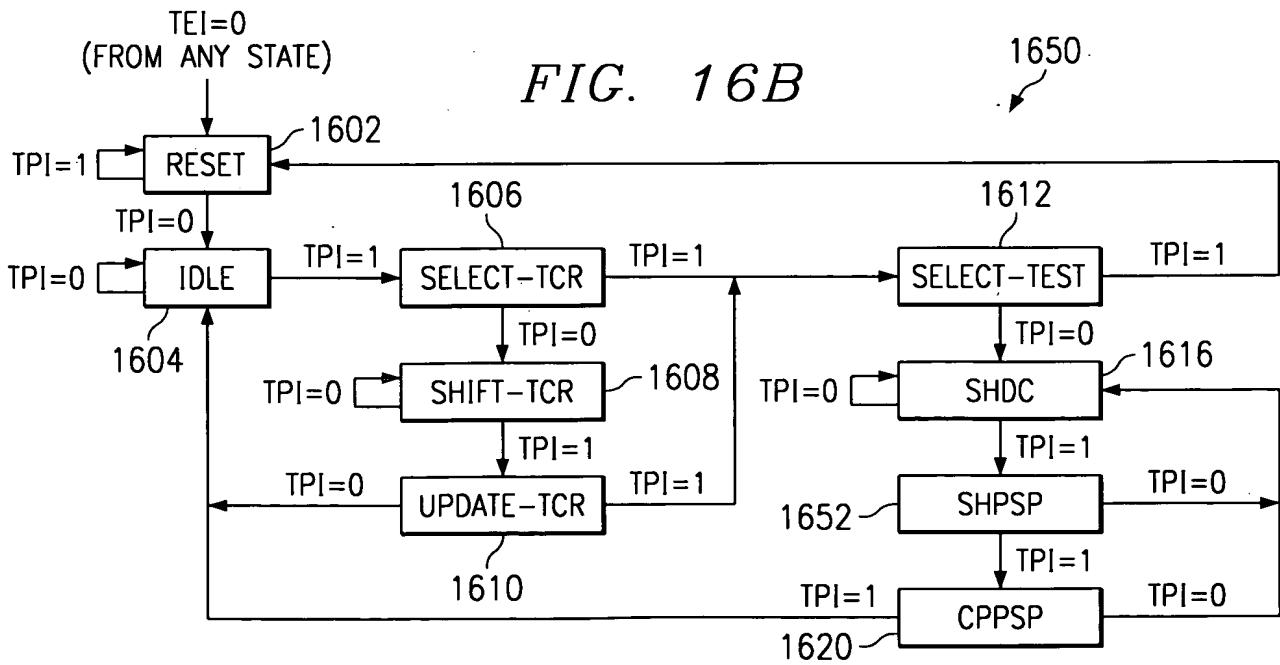


FIG. 15B



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FIG. 16A

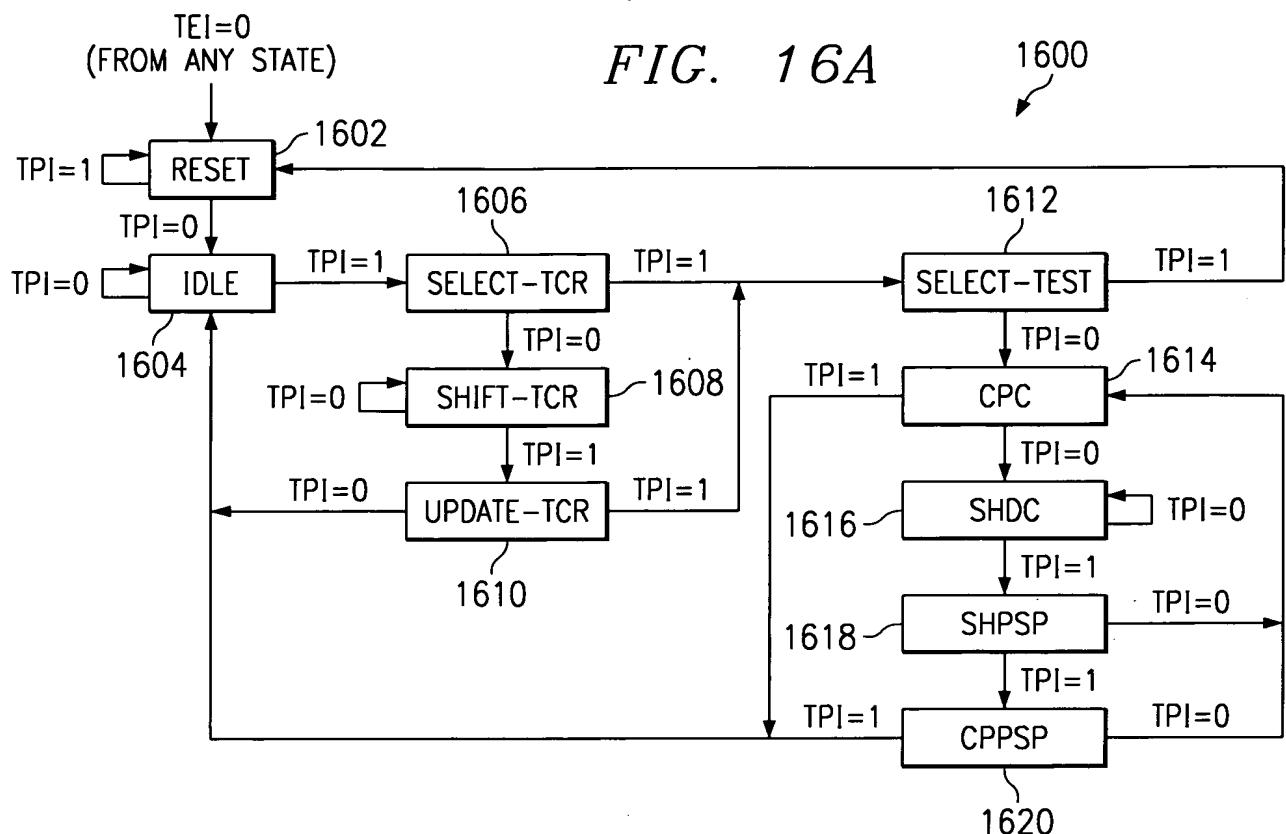
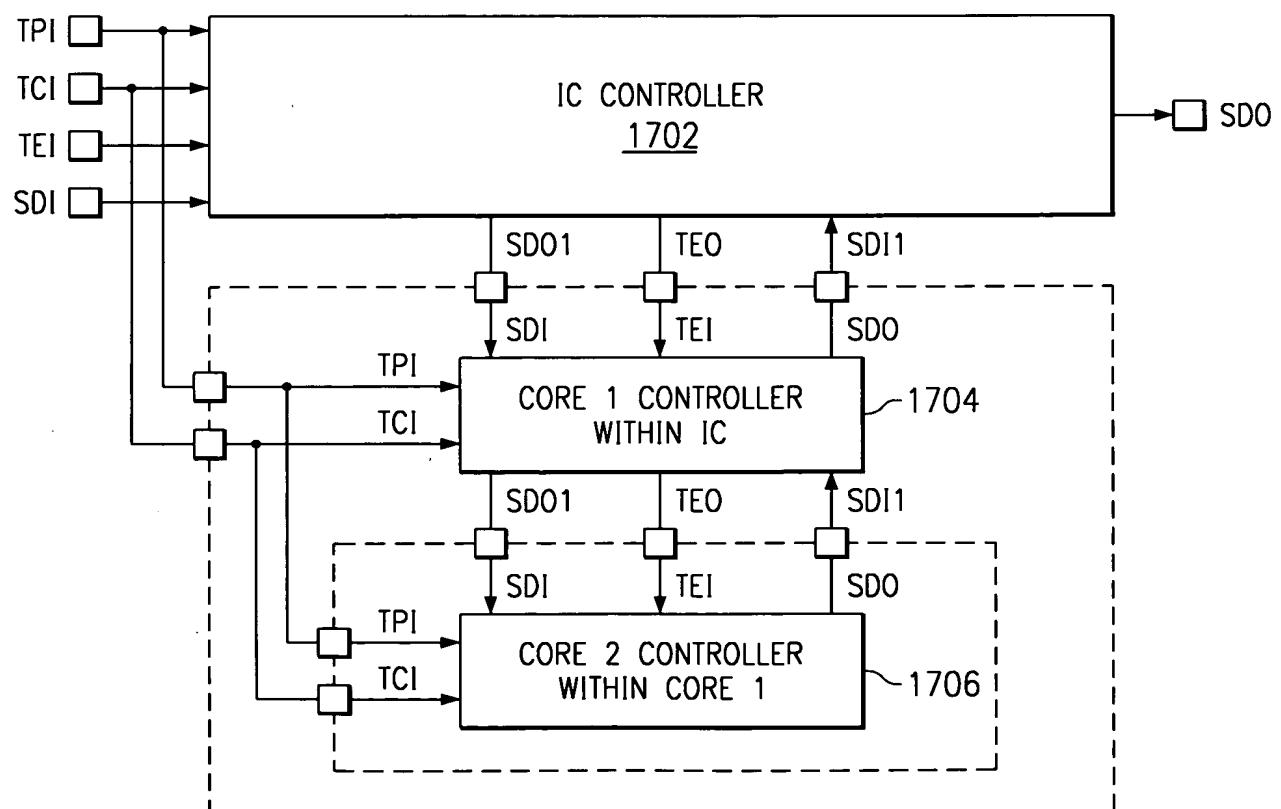


FIG. 17



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FIG. 18

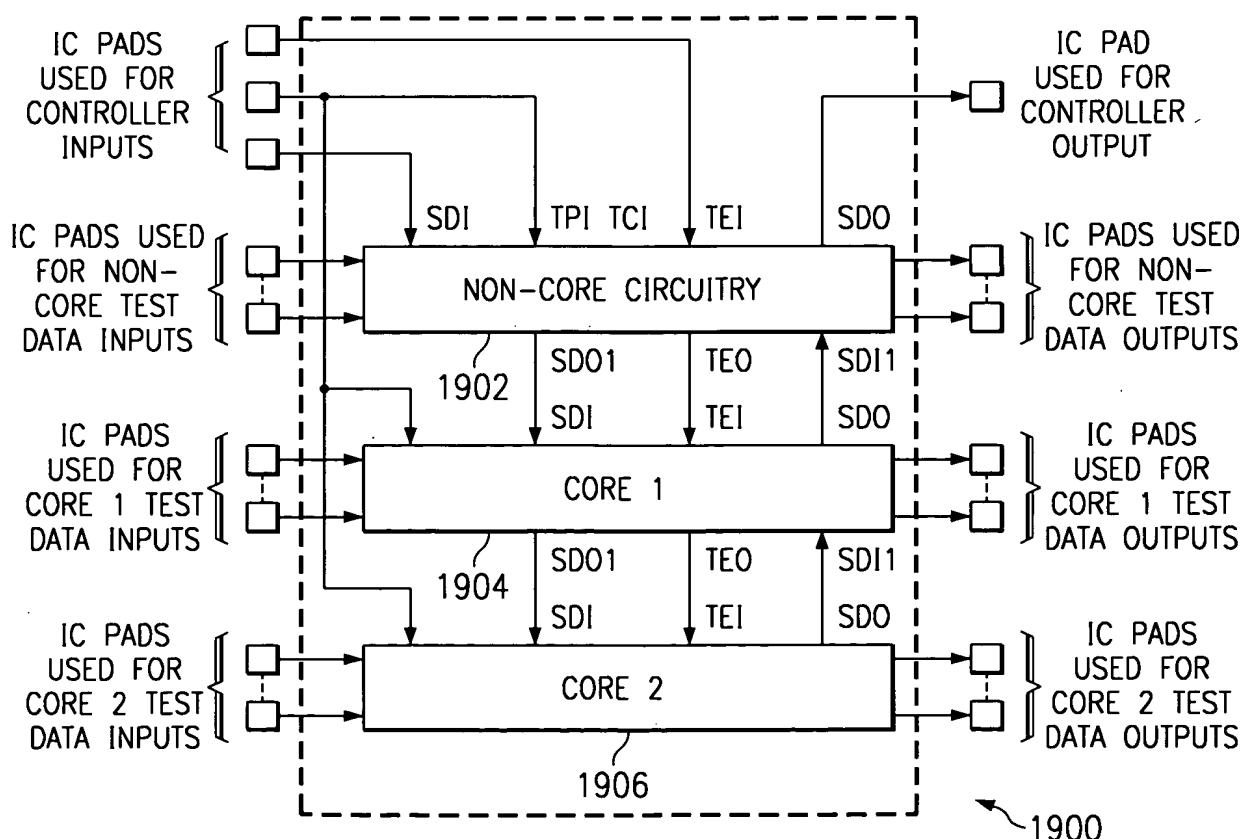
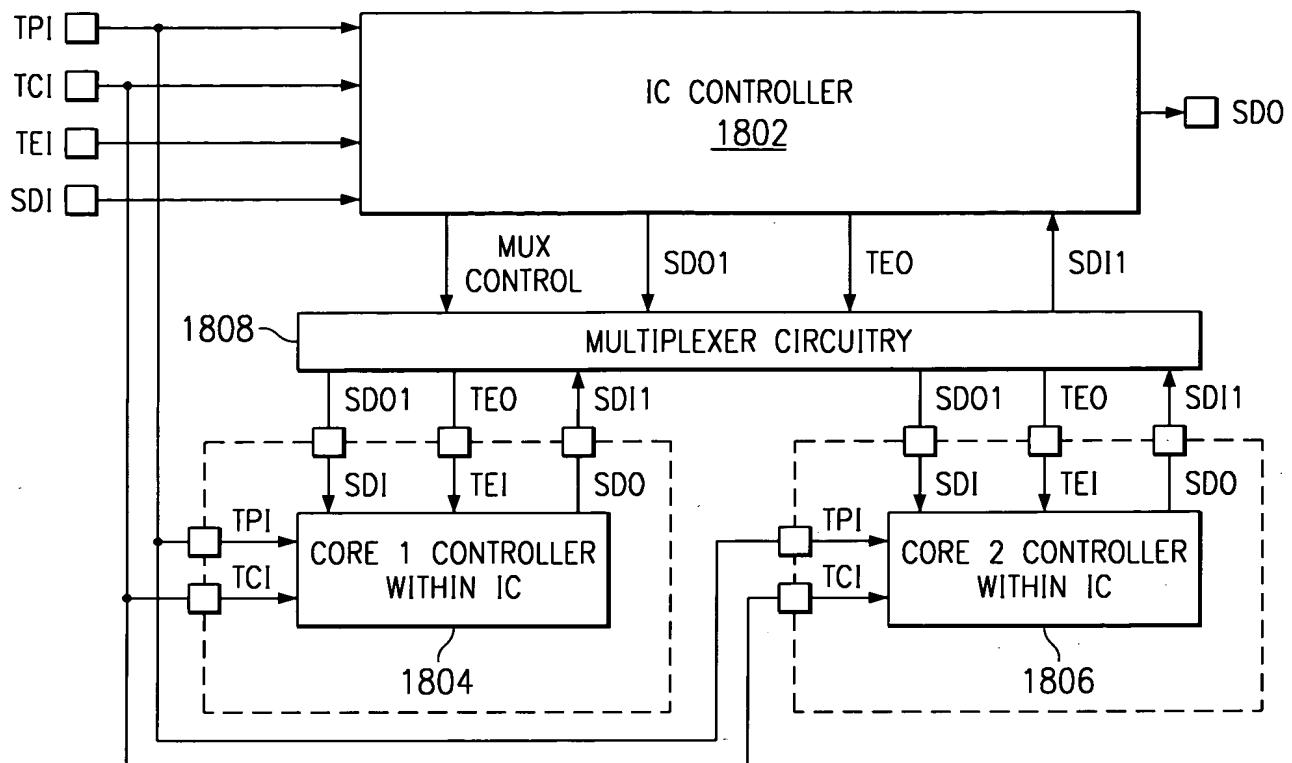
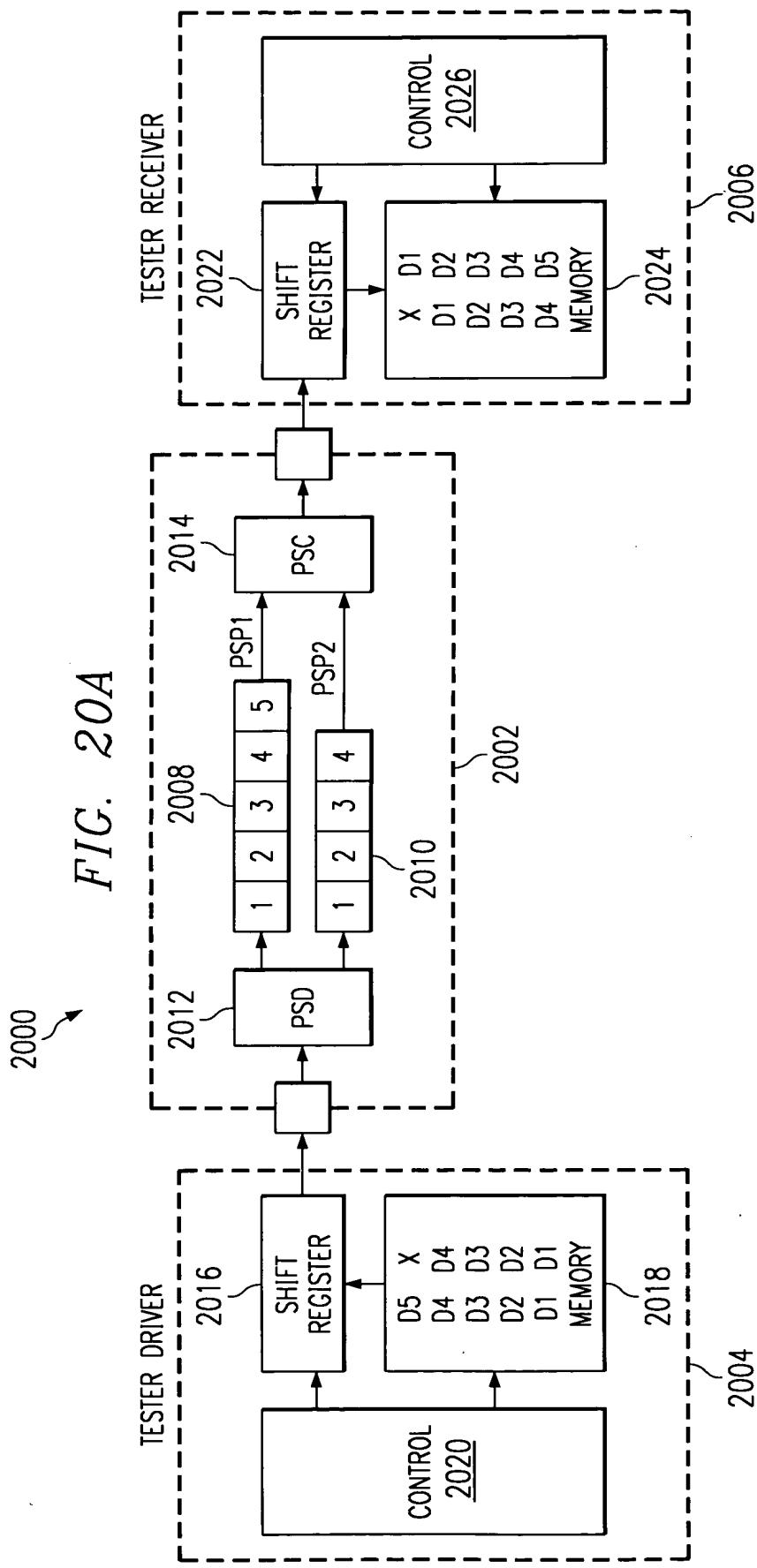


FIG. 19



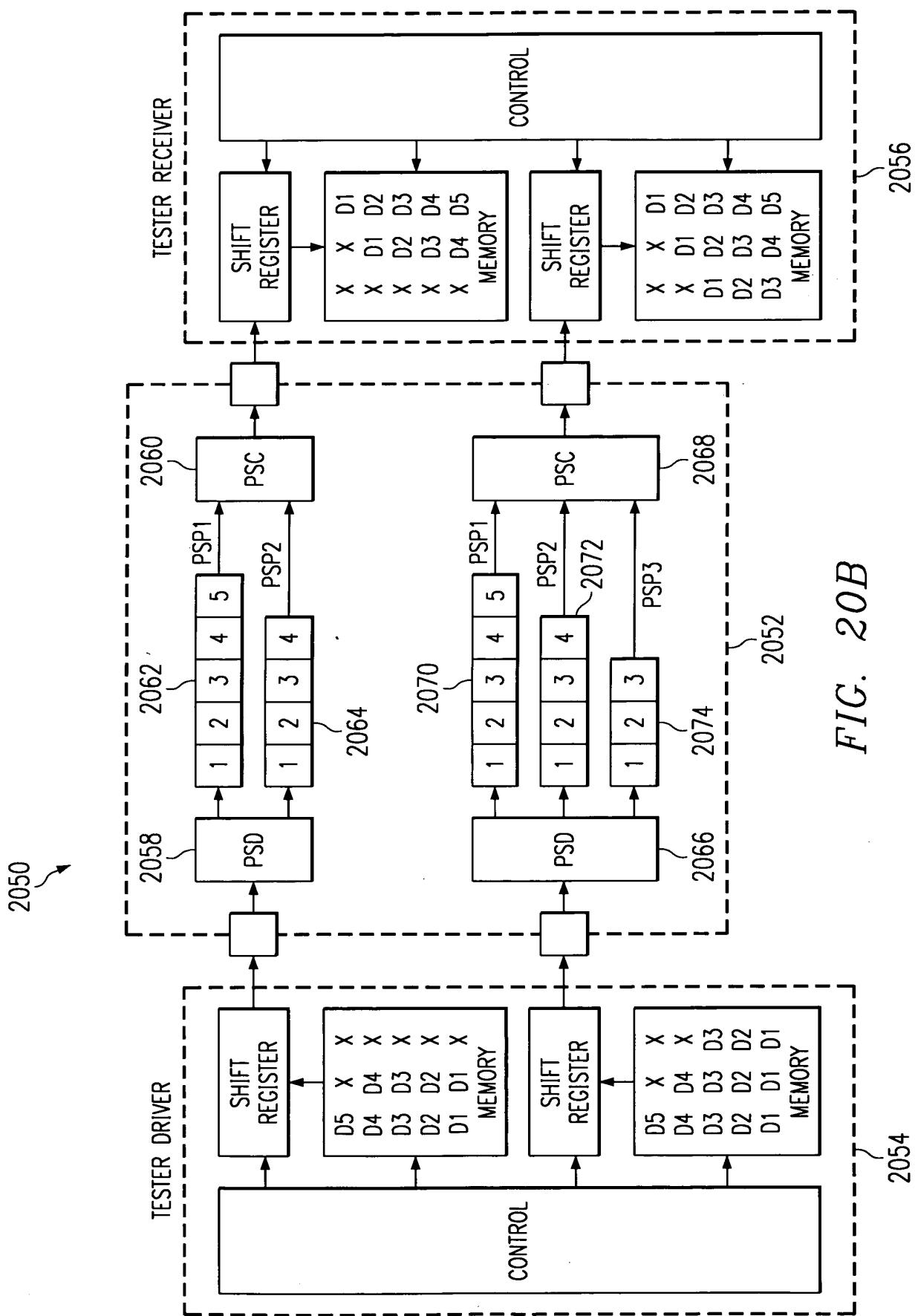
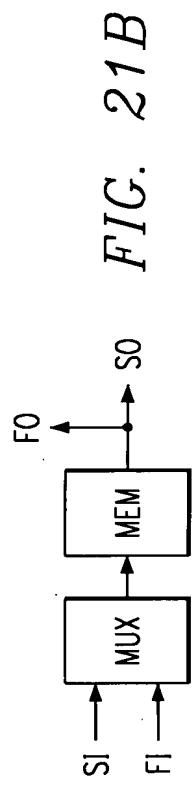
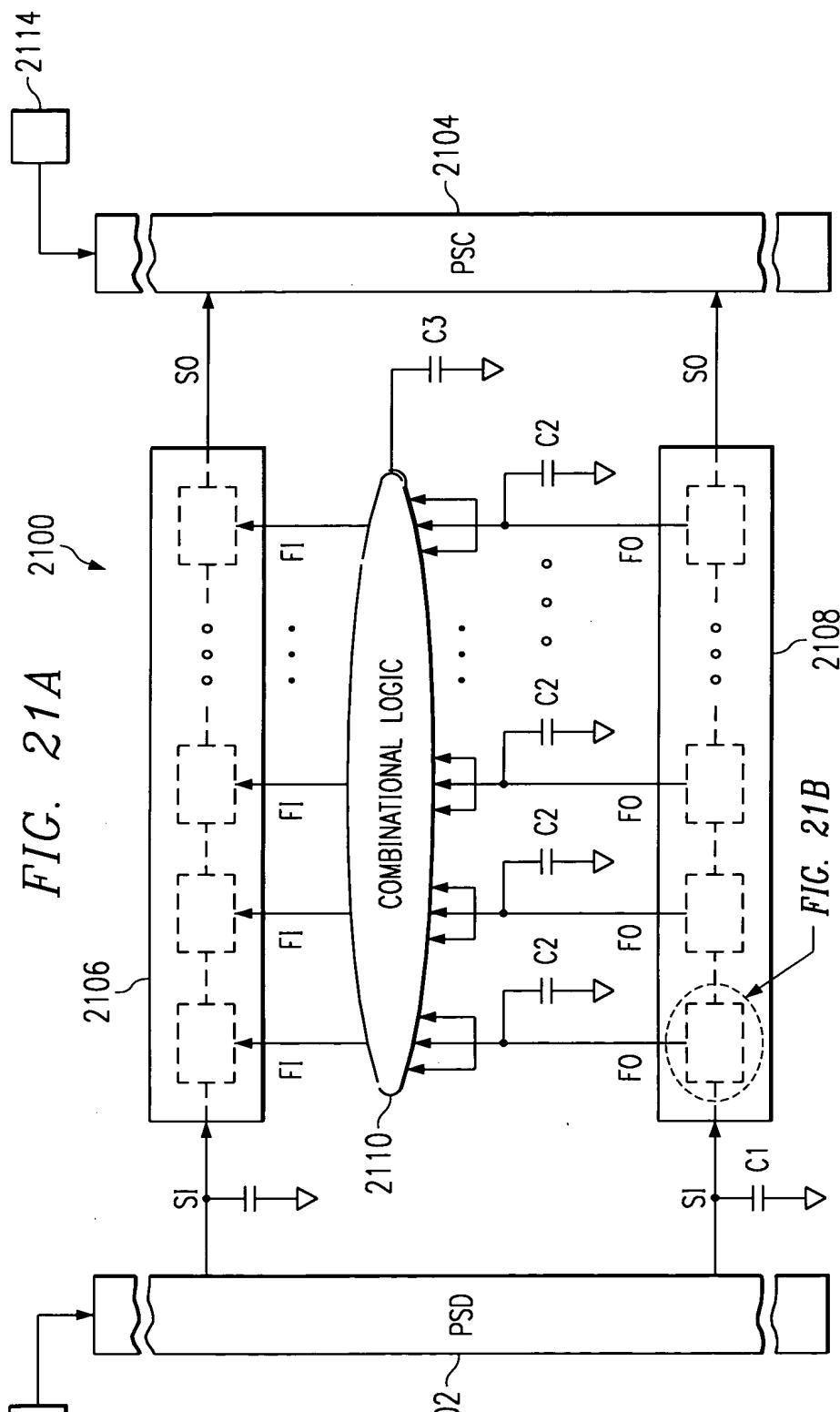


FIG. 20B

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2056



The diagram illustrates a circuit labeled FIG. 22A. It features a central 'SYNCHRONIZER' block (2202) with two inputs: 'SHPSP' and 'SIGNAL'. The 'SIGNAL' input is connected to the 'SIGNAL' input of a first PSP stage (2204). The 'SHPSP' input is connected to the 'SHPSP' input of a second PSP stage (2206). The 'SIGNAL' output of the first stage is connected to the 'SIGNAL' input of the second stage. The 'SHPSP' output of the second stage is connected to the 'SHPSP' input of a third PSP stage (2212). The 'SIGNAL' output of the second stage is connected to the 'SIGNAL' input of the third stage. The 'SHPSP' output of the third stage is connected to the 'SHPSP' input of a fourth PSP stage (2214). The 'SIGNAL' output of the third stage is connected to the 'SIGNAL' input of the fourth stage. The 'SIGNAL' output of the fourth stage is labeled '2200'. The four PSP stages (2204, 2206, 2212, 2214) are connected in parallel. The outputs of the four PSP stages are labeled 'PSP 1', 'PSP 2', 'PSP 3', and 'PSP 10' respectively. The outputs of PSP 3 and PSP 10 are connected in parallel, with a dashed line indicating the continuation of the signal path. The outputs of PSP 2 and PSP 10 are also connected in parallel, with a dashed line indicating the continuation of the signal path.

FIG. 22B

SIGNAL

PSP 1

PSP 2

PSP 3

PSP 10

SHPSP

FIG. 22B

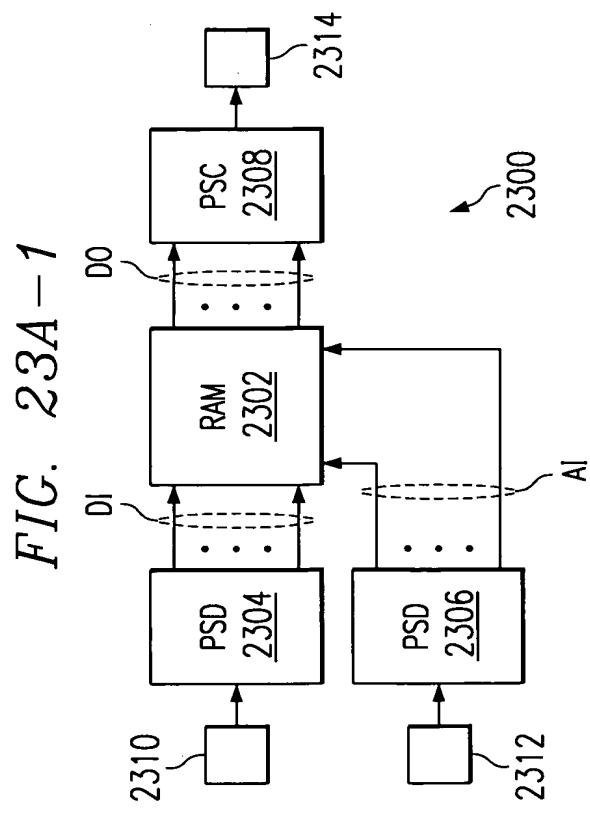
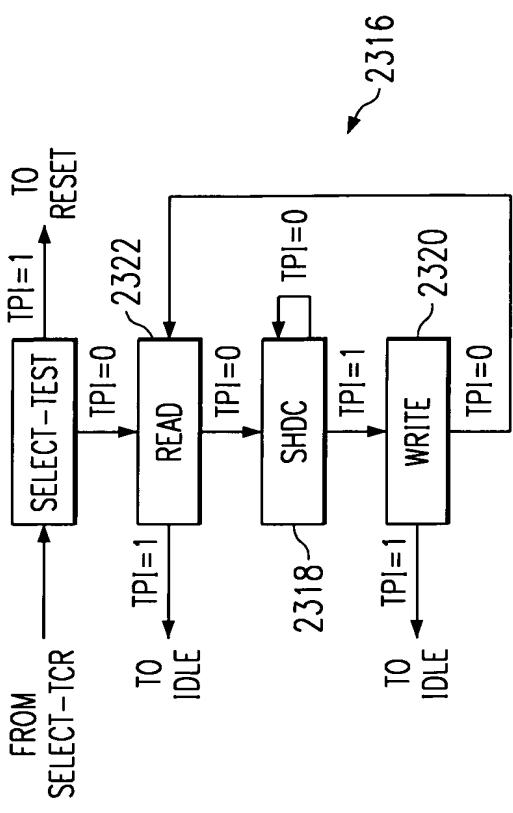


FIG. 23A-1

FIG. 23A-2



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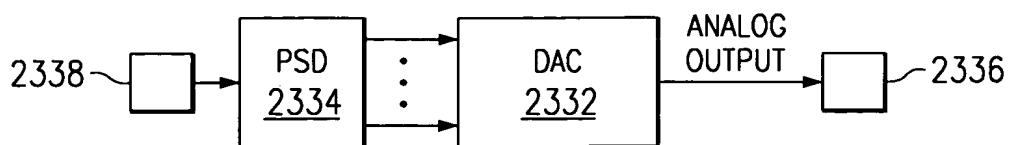


FIG. 23B-1

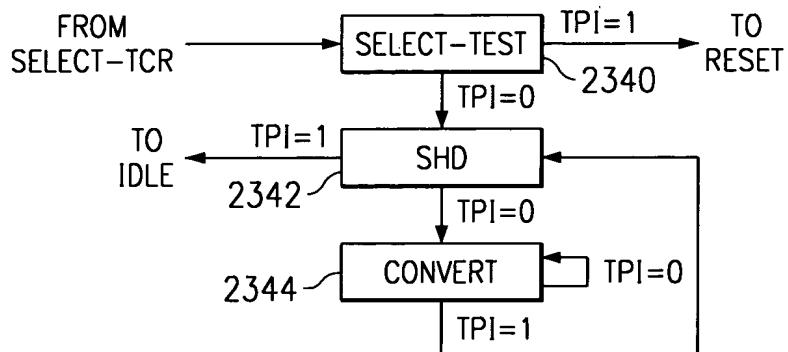


FIG. 23B-2

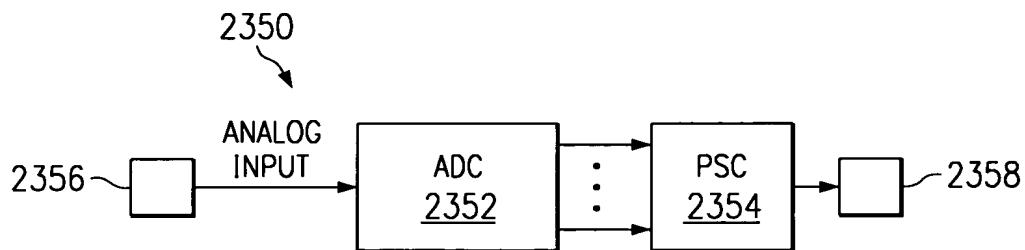


FIG. 23C-1

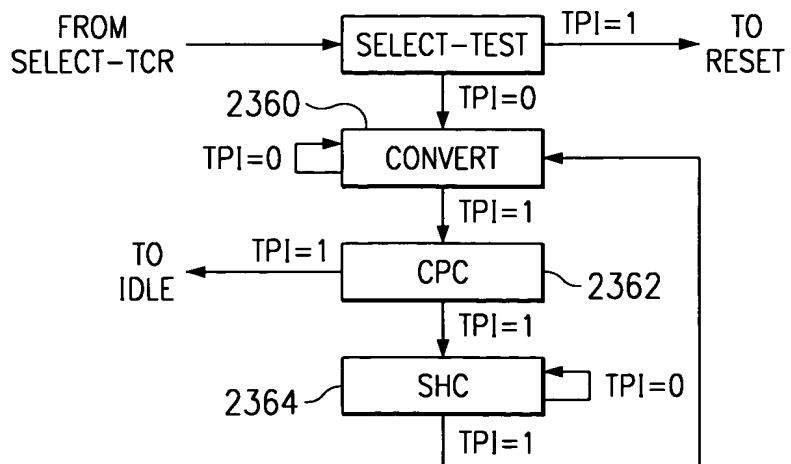
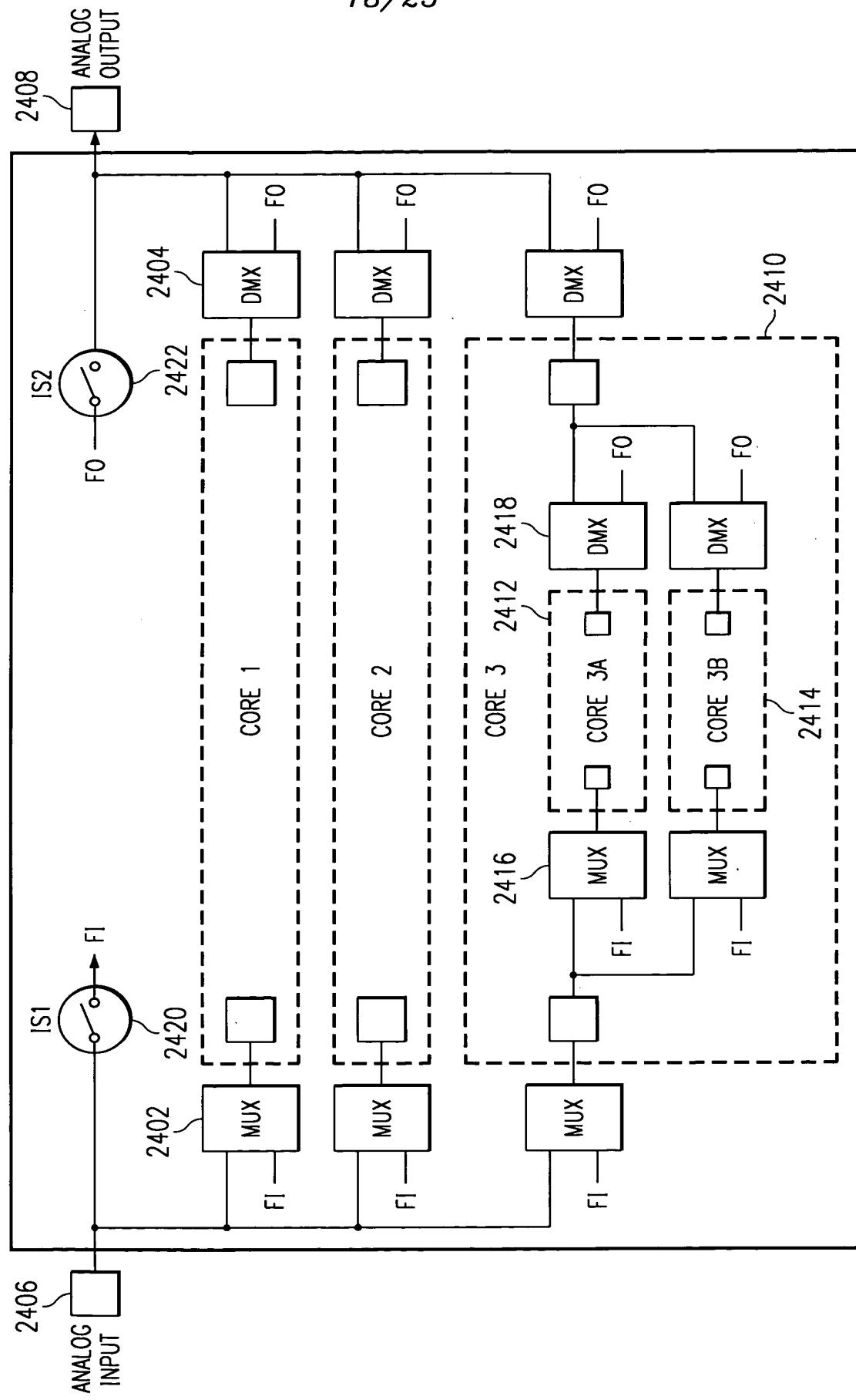


FIG. 23C-2

FIG. 24



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FIG. 25A

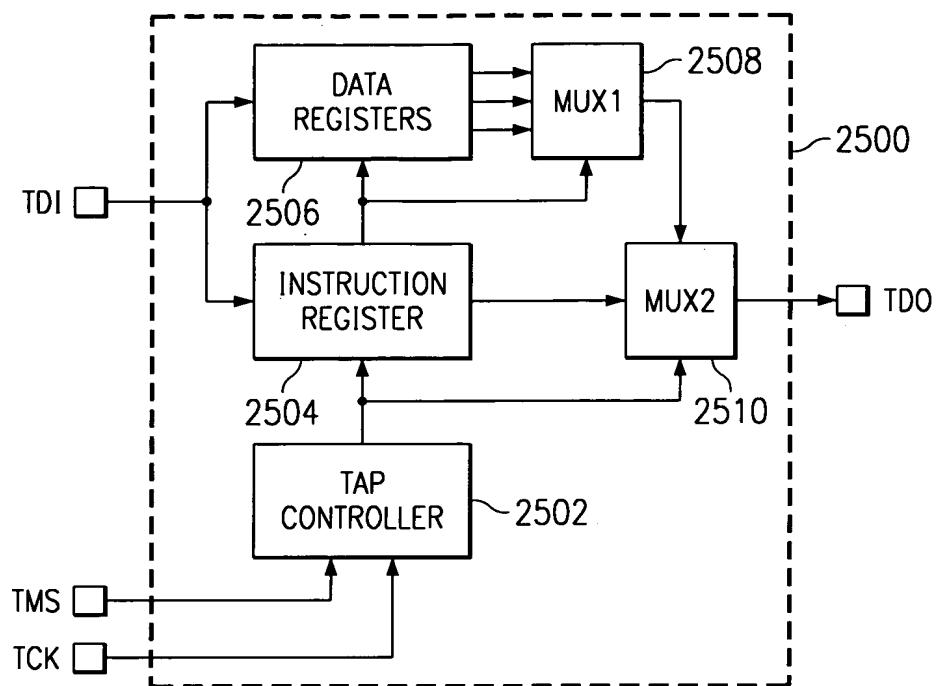
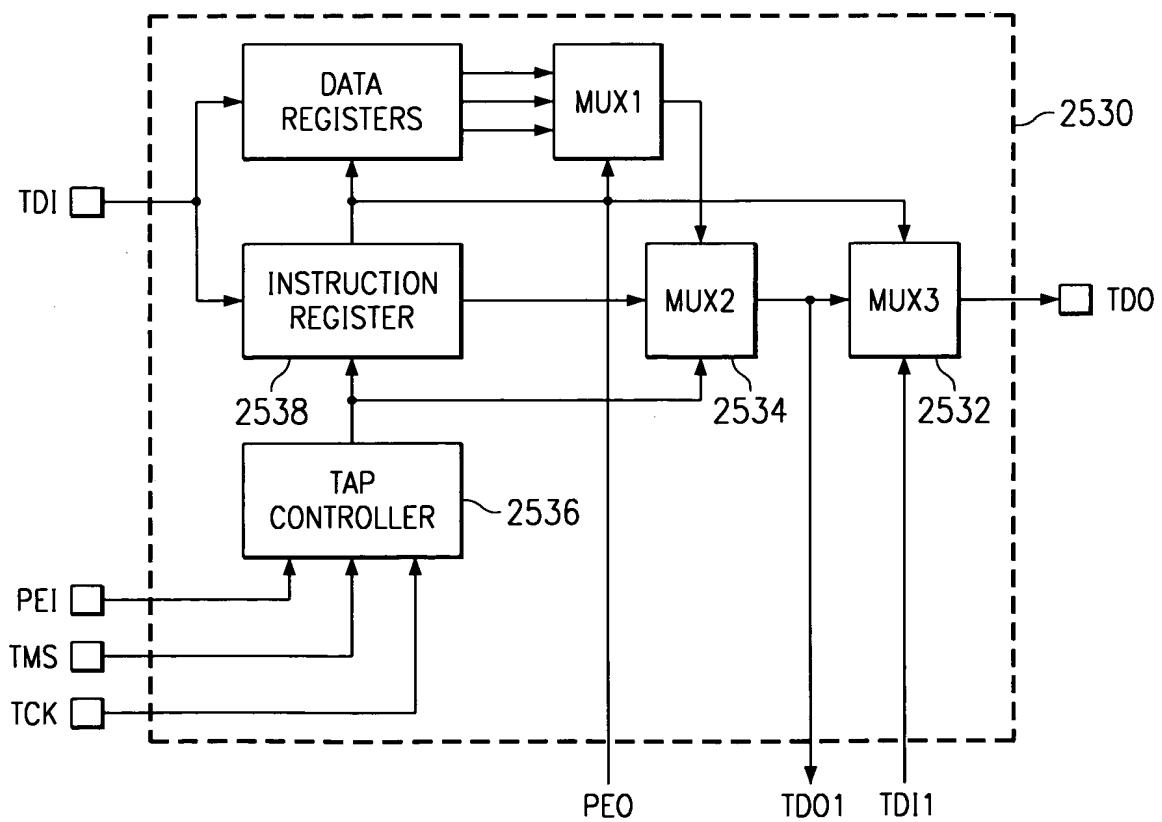


FIG. 25B



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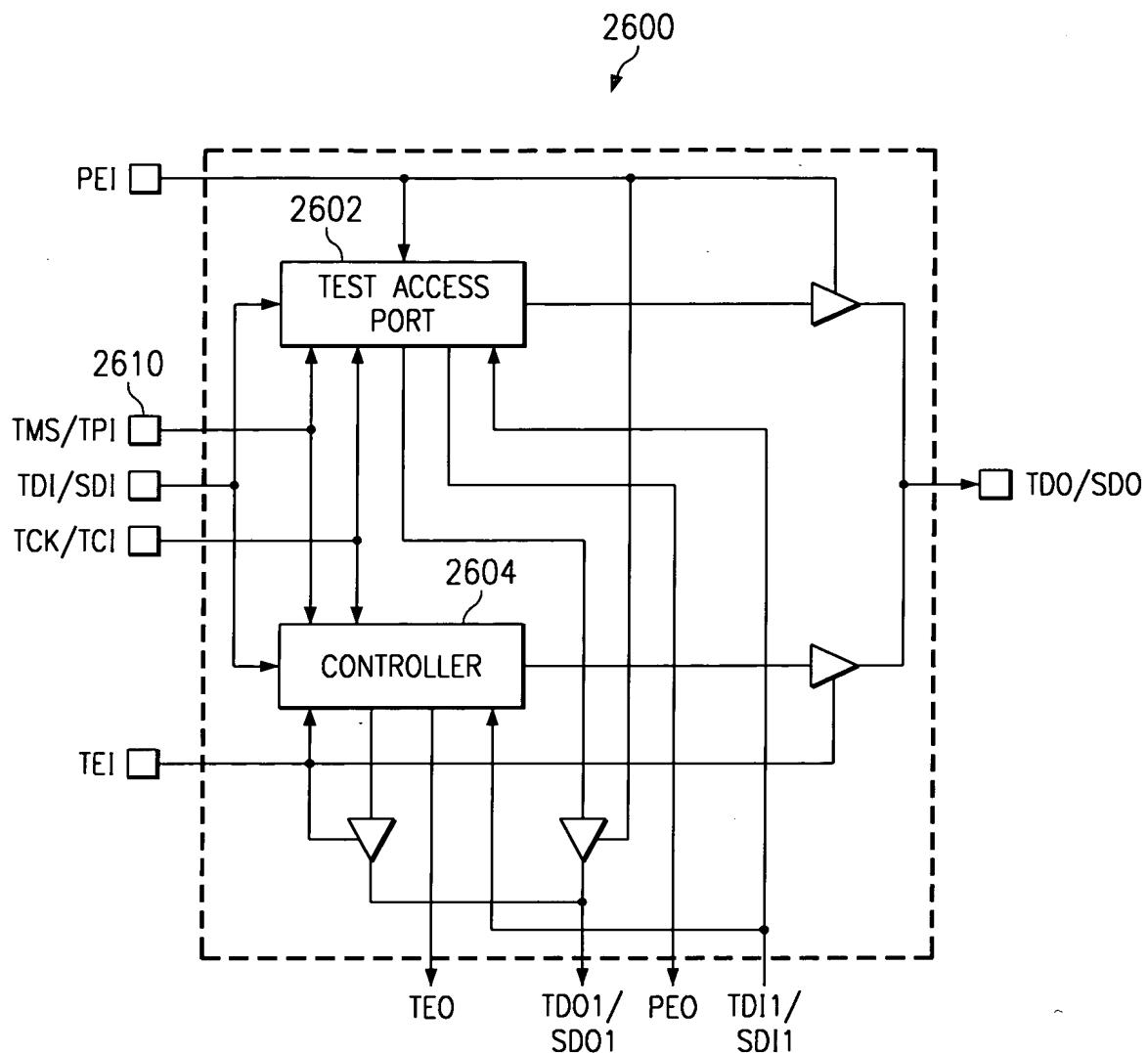


FIG. 26

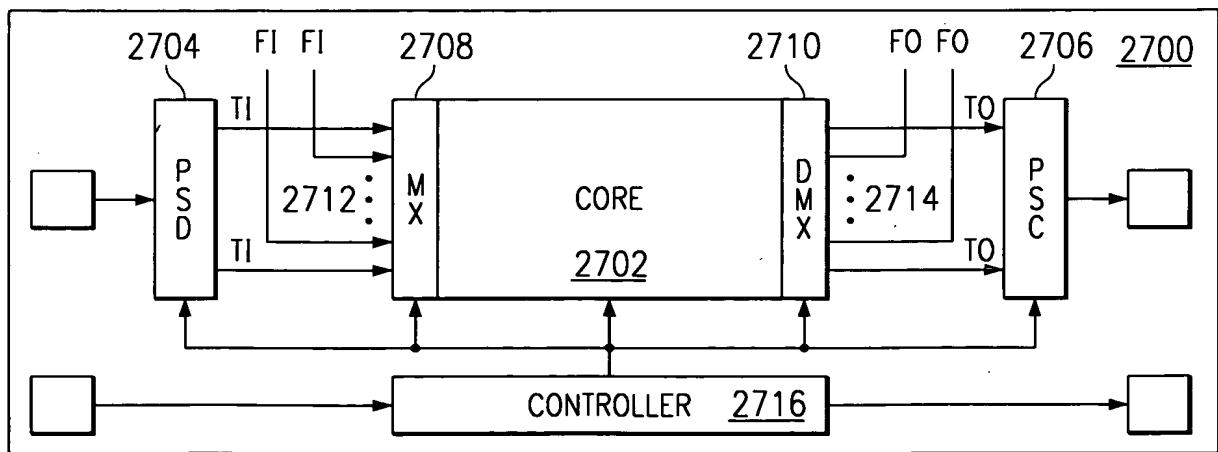


FIG. 27

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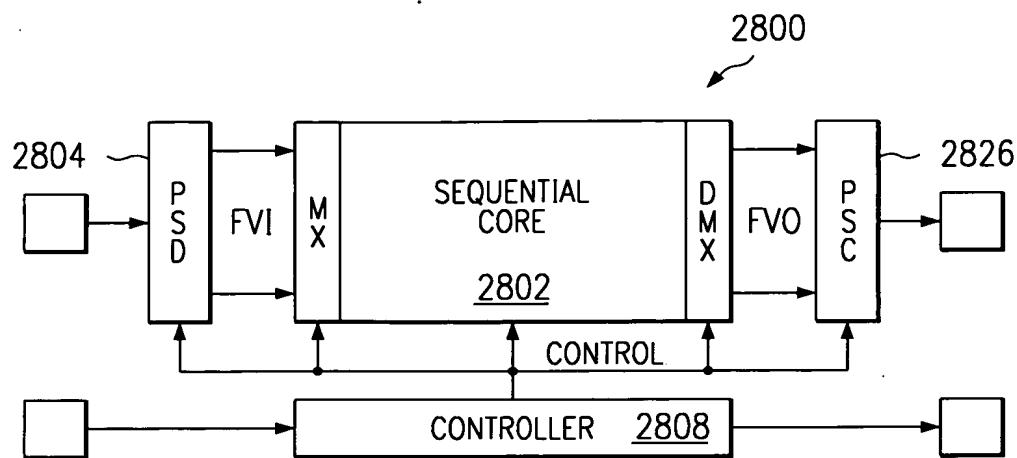


FIG. 28A-1

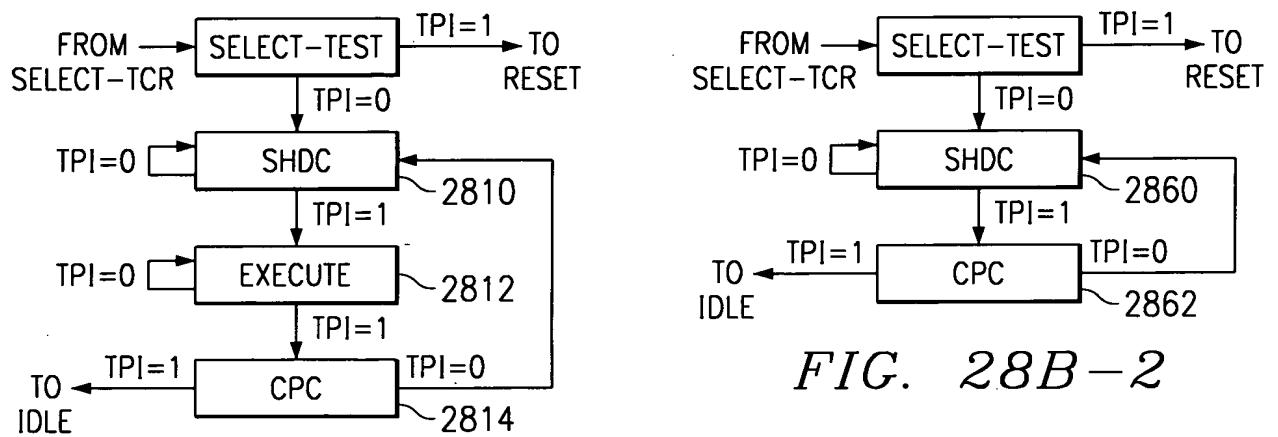


FIG. 28A-2

FIG. 28B-2

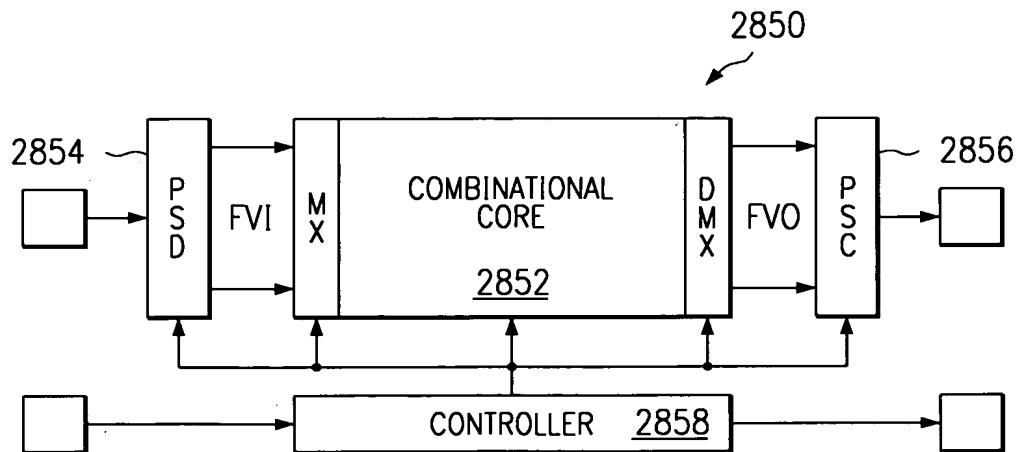


FIG. 28B-1

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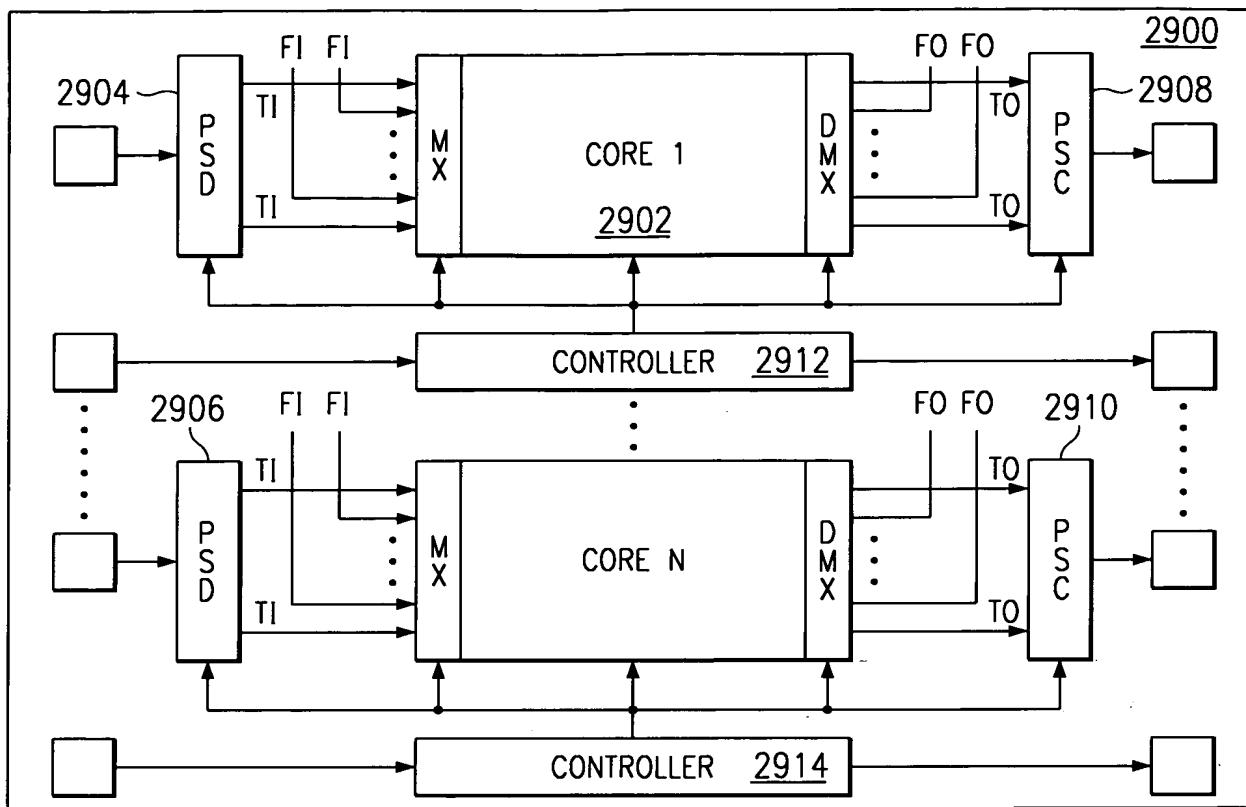


FIG. 29

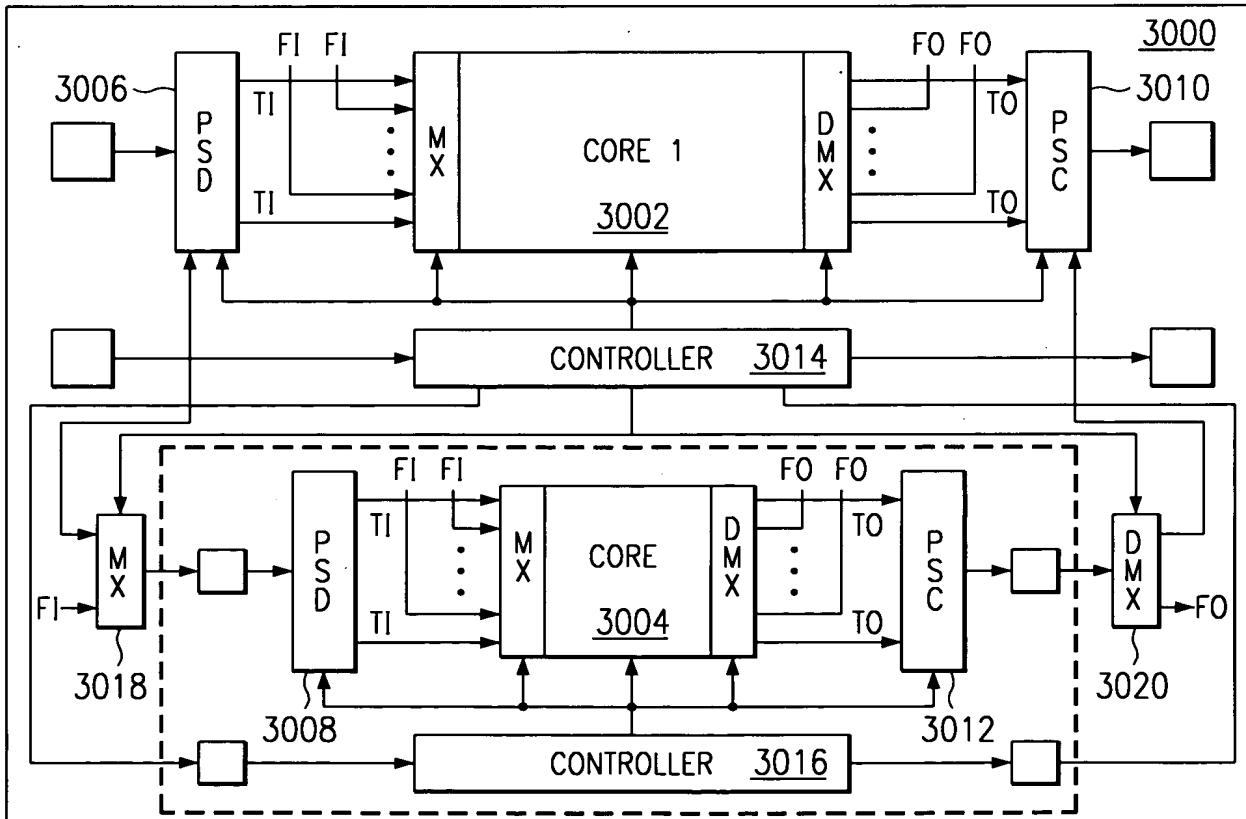


FIG. 30

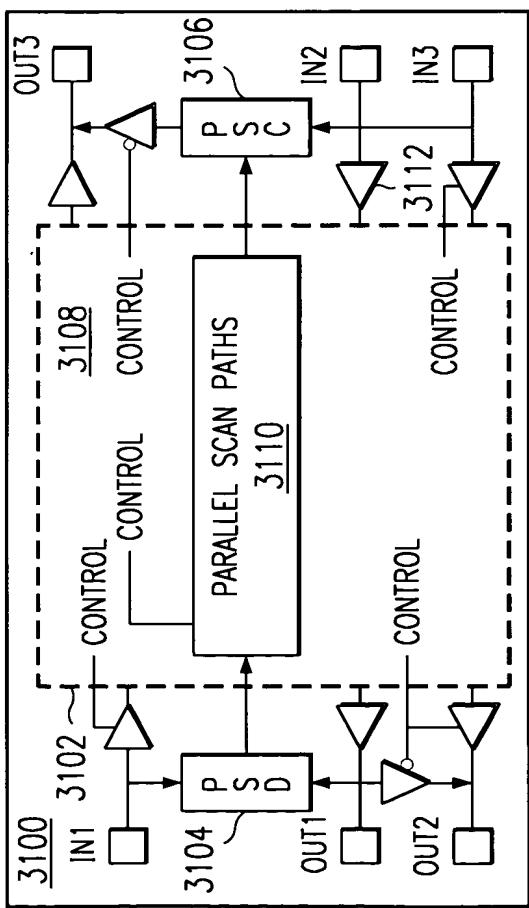


FIG. 31A

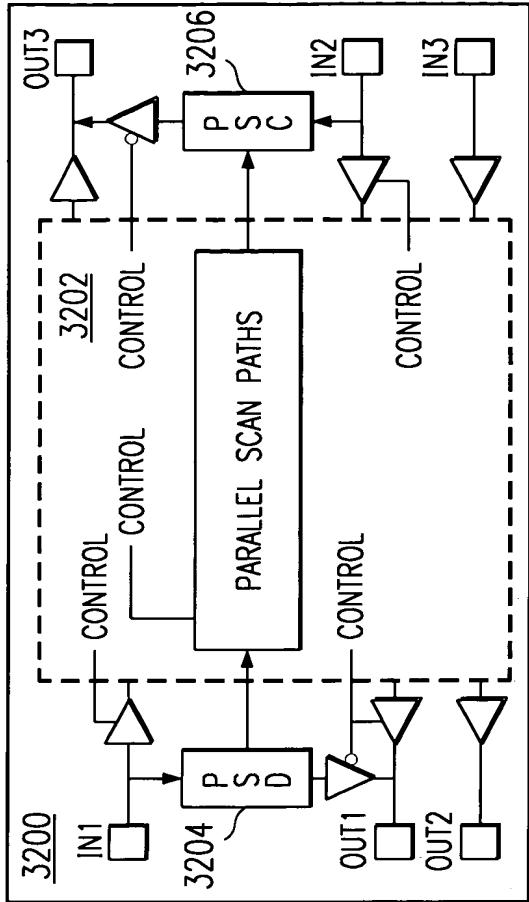


FIG. 32A

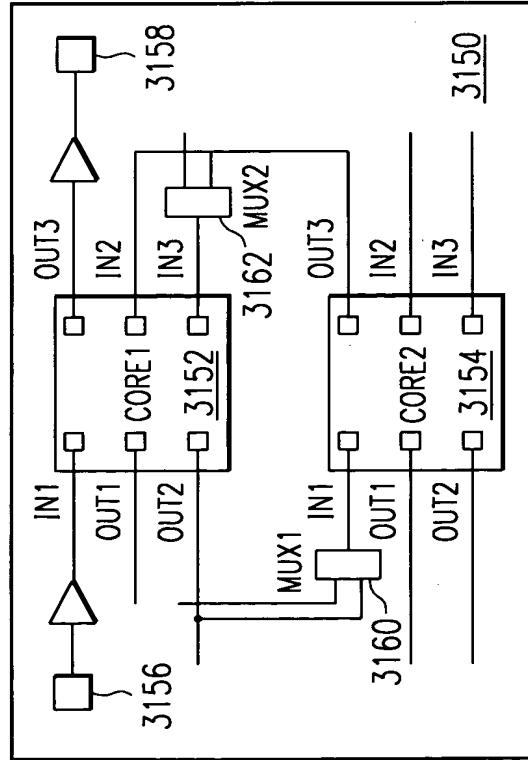


FIG. 31B

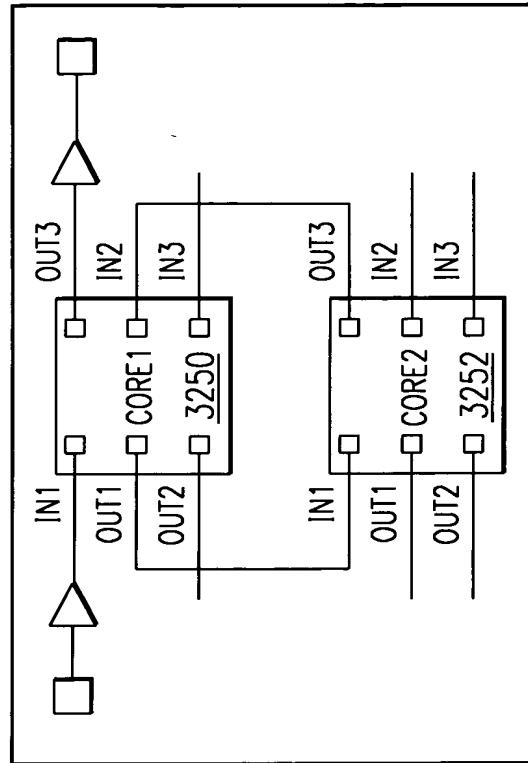


FIG. 32B

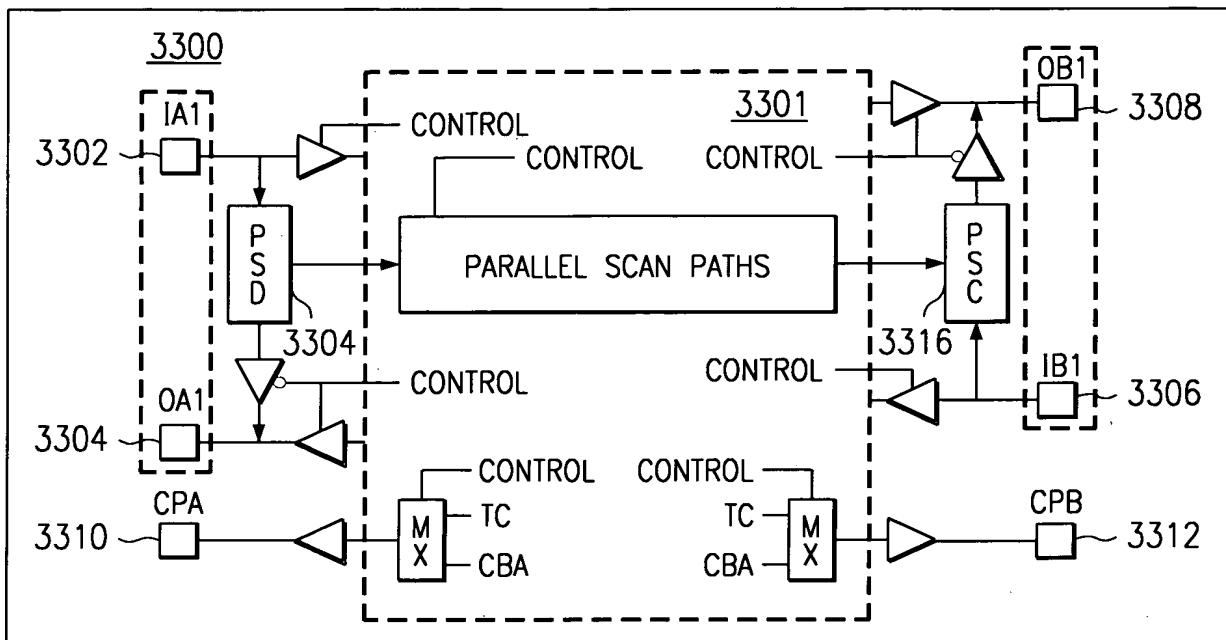


FIG. 33A

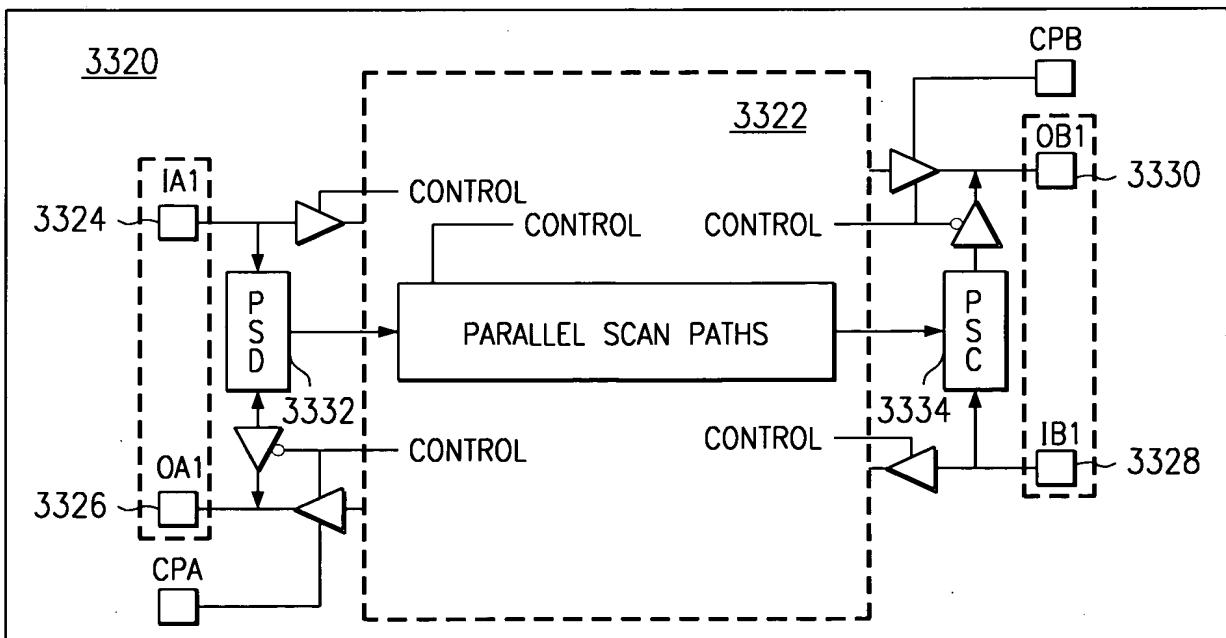


FIG. 33B

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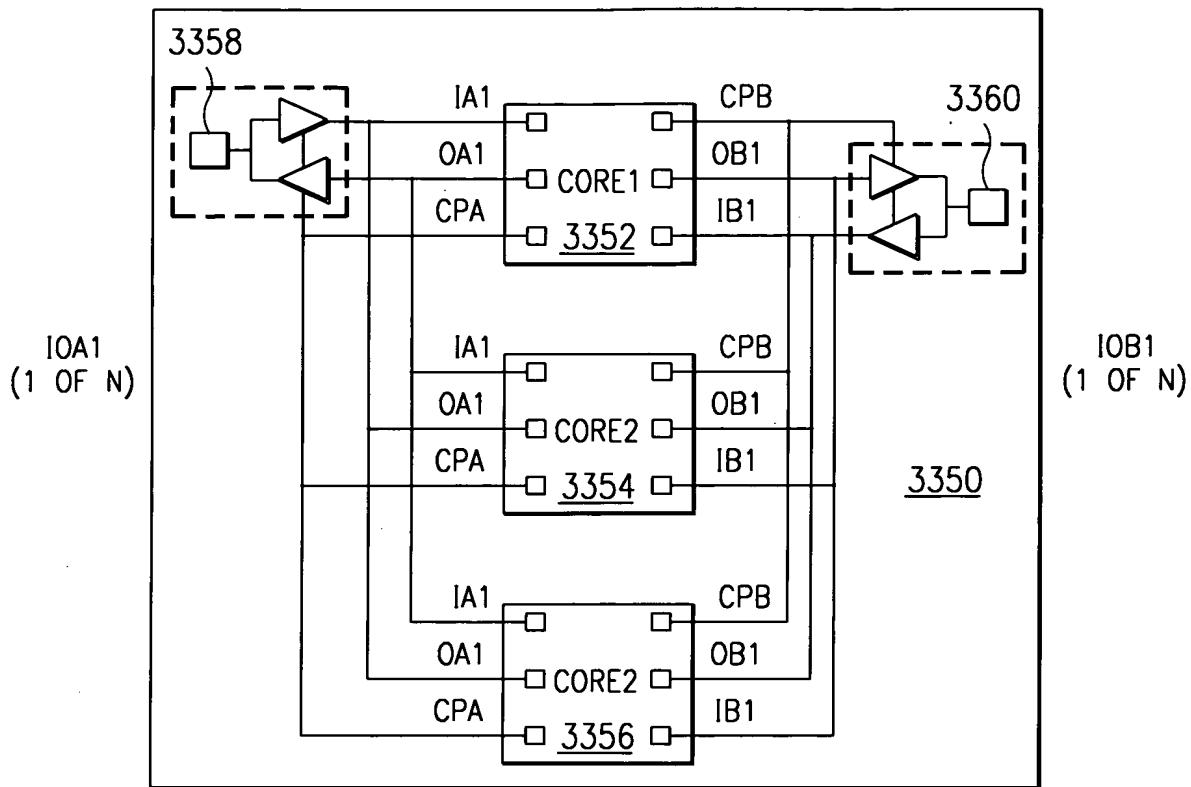


FIG. 33C

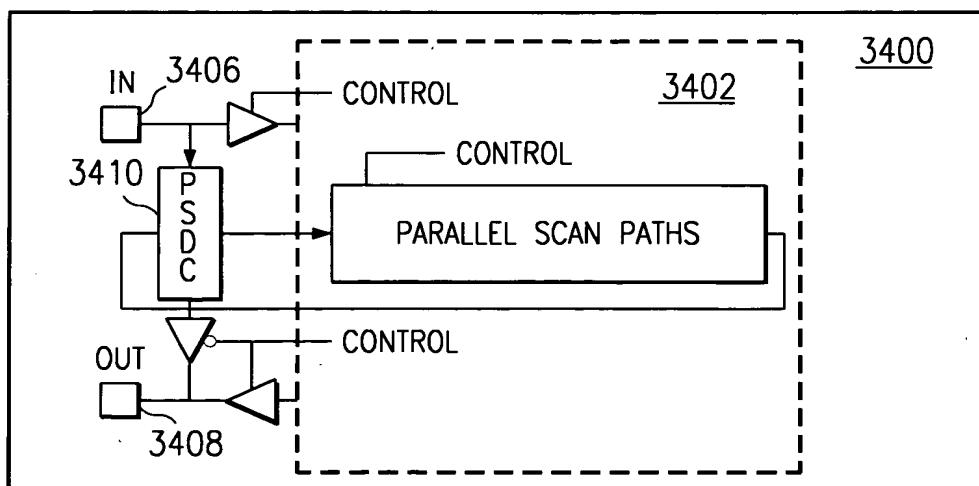


FIG. 34